

Opt-Pass Sensor

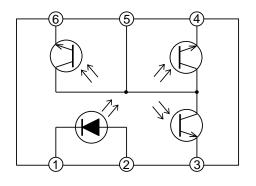
■FEATURES

- Built-in function of visible light cut-off filter
- Miniature, thin package: 2.3mm × 2.0mm × 0.6mm
- Pb free soldering re-flowing permitted: 255°C, 2times
- Halogen free, Pb free
- Compliant with RoHS directive

■APPLICATION

- Detecting the location of AF Lens unit for DSC, DVC
- Detecting the location of Zoom Lens unit for DSC,DVC
- Positioning for high precision

■EQUIVALENT CIRCUIT-BLOCK DIAGRAM



■GENERAL DESCRIPTION

The NJL9101R is the high-precision optical position sensor, which is combining with a super luminosity RED LED and Si light-sensitive element into small package. Three sin waves with high precision phase difference will output by using the dedicated striped mirror. By arithmetic processing of the sensor output signal, the position detection with high precision and high resolution can be performed.

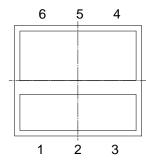
The characteristics by optical scheme are not affected from the magnetic field of magnet that built into the motor etc The Opt-Pass sensor has been designed strongly against the distance variation between the sensor and the striped mirror. Also the Opt-pass has been designed strongly against the tilt variation.

- 1. Cathode (LED)
- 2. Anode (LED)
- 3. Emitter1 (Out1)
- 4. Emitter2 (Out2)
- 5. Collector (Common Emitter1,2,3)
- 6. Emitter3 (Out3)

■PIN CONFIGURATION

	PIN No	NAME	FUNCTION
	1	Cathode (LED)	Cathode for LED
	2	Anode (LED)	Anode for LED
	3	Emitter1 (Out1)	Emitter1 for Photo Tr
	4	Emitter2 (Out2)	Emitter2 for Photo Tr
	5	Collector	Collector (Common Emiter1,2,3)
	6	Emitter3 (Out3)	Emitter3 for Photo Tr
_			





■ORDERING INFORMATION

PART NUMBER	PACKAGE OUTLINE	RoHS	HALOGEN- FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ(pcs)
NJL9101R	COBP	yes	yes	Au	No marking	4.5	2,000



■ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATING	UNIT			
Emitter						
Forward Current (Continuous)	l _F	15	mA			
Reverse Voltage (Continuous)	V _R	6	V			
Power Dissipation	P _D	45	mW			
Detector						
Collector-Emitter Voltage	VCEO	16	V			
Emitter-Collector Voltage	V _E CO	6	V			
Collector Current	Ic	10	mA			
Collector Power Dissipation	Pc	25	mW			
Coupled						
Total Power Dissipation	P _{tot}	60	mW			
Operating Temperature	T _{opr}	-30 to +70	°C			
Storage Temperature	T _{stg}	-30 to +85	°C			
Reflow Soldering Temperature	T _{sol}	255	°C			

■RECOMMENDATION OPERATING CONDITION

PARAMETER	SYMBOL	VALUE	UNIT
Forward current	l _F	5	mA
Collector-Emitter Voltage	VCEO	+2.0 to +3.3	V
Distance between sensor and reflector	Gap	0.7	mm

■ELECTRO-OPTICAL CHARACTERISTICS DC (Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Emitter							
Forward Voltage	VF	I=5mA	1.1	1.4	1.7	V	
Revers Voltage	I _R	V _R =6V	-	-	10	μΑ	
Peak wavelength	λ_{P}		-	850	-	nm	
Detector	Detector						
Dark Current	ICEO	V _{CE} =5V, In the dark	-	-	0.5	μA	
Collector-Emitter Voltage	Vce	I _C =100μA, In the dark	16	-	-	V	
Coupled							
Output Current 1	lo1	I _F =5mA,V _{CE} =3.3V,d=0.7mm(Al Mirror) *1	153	300	550	μA	
Output Current 2	l _o 2	I=5mA,Vc=3.3V,d=0.7mm(Al Mirror) *1	153	300	550	μΑ	
Output Current 3	l _o 3	I=5mA,Vc=3.3V,d=0.7mm(Al Mirror) *1	153	300	550	μΑ	
Operating Dark Current 1	I _{CEOD} 1	I _F =5mA,V _{CE} =3.3V	-	-	0.5	μA	
Operating Dark Current 1	I _{CEOD} 2	I _F =5mA,V _{CE} =3.3V	-	-	0.5	μA	
Operating Dark Current 1	ICEOD3	I=5mA,Vc=3.3V	-	-	0.5	μA	

^{*1} Output Current is the value obtained that is used the reflective board of Aluminum Evaporation

Design Guarantee Item. Items that are showed only the typical value are not tested in manufacturing process.



■ELECTRO-OPTICAL CHARACTERISTICS AC (Ta=25°C)

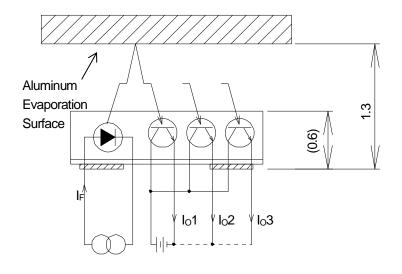
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Coupled						
Output Voltage 1	V ₀ 1	I _F =5mA,V _{CE} =3.3V,RL=2kΩ,d=0.7mm *2	_	90		mVp-p
Output Voltage 2	V ₀ 2	I _F =5mA,V _{CE} =3.3V,RL=2kΩ,d=0.7mm *2	_	90		mVp-p
Output Voltage 3	V _O 3	I _F =5mA,V _{CE} =3.3V,RL=2kΩ,d=0.7mm *2	_	90		mVp-p
Phase Difference 2	Vo2p	Phase Difference to Output Voltage 1	_	90		deg
Phase Difference 3	Vo3 _P	Phase Difference to Output Voltage 1	_	180	_	deg
Cut-off Frequency	f _C	V_{CE} =3.3 V ,RL=2 $k\Omega$,-3 dB	_	20		kHz

^{*2} Output Voltage is the value obtained that is used the dedicated striped mirror (Metal type: Meltec Corporation).

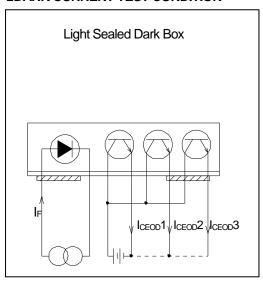
Design Guarantee Item. Items that are showed only the typical value are not tested in manufacturing process.

The minimum and maximum values are reference values.

■OUTPUT CURRENT TEST CONDITION

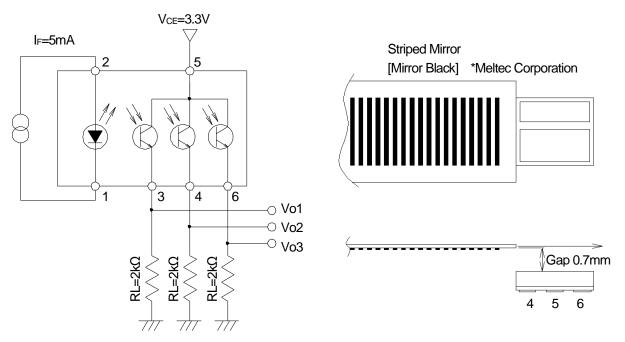


■DARK CURRENT TEST CONDITION

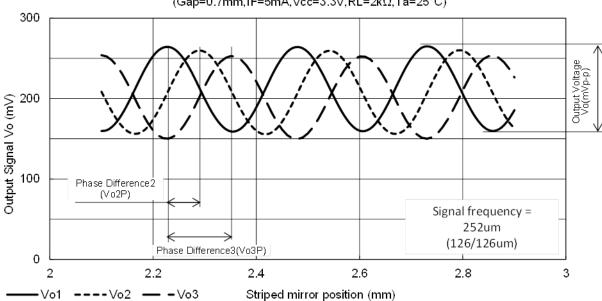




■OUTPUT VOLTAGE TEST CONDITION



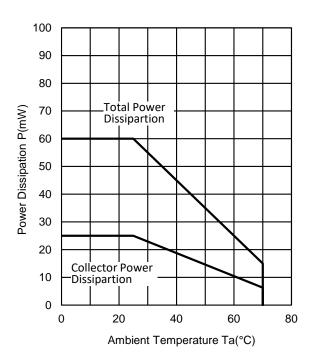
Output Characteristics of Striped mirror (Mirror Black @126um/126um) (Gap=0.7mm,IF=5mA,Vcc=3.3V,RL=2k Ω ,Ta=25°C)



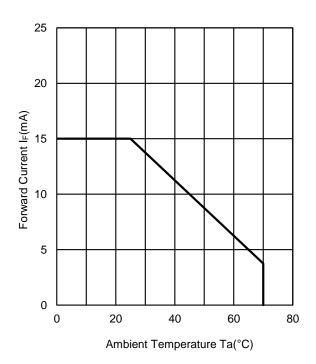


■POWER DISSIPATION vs. AMBIENT TEMPERATUR

Power Dissipation vs. Temperature



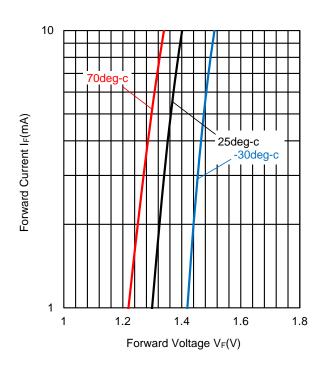
Forward Current vs. Temperature



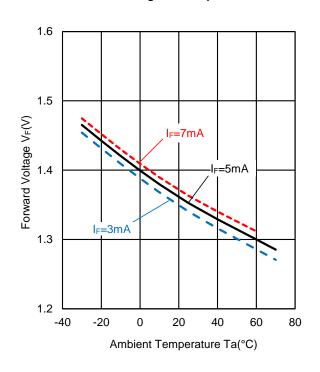


■TYPICAL CHARACTERISTICS (Ta=25°C)

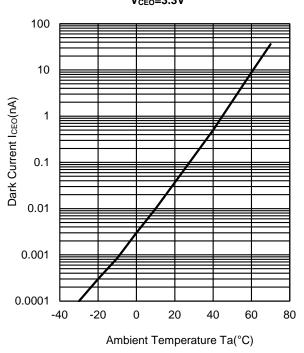
Forward Voltage vs. Forward Current



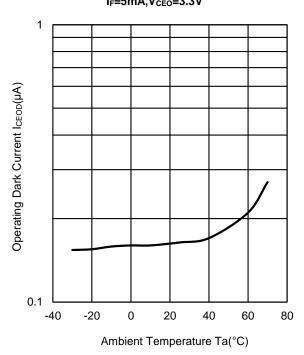
Forward Voltage vs. Temperature



Dark Current vs. Temperature V_{CEO}=3.3V

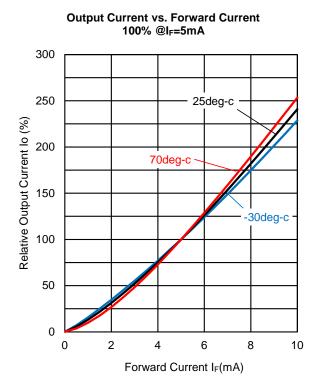


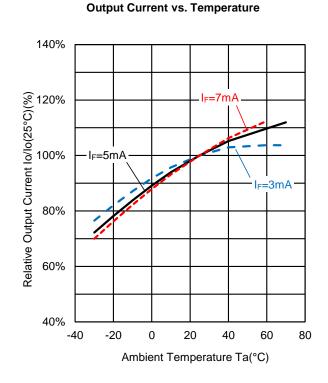
Operating Dark Current vs. Temperature I_F=5mA,V_{CEO}=3.3V

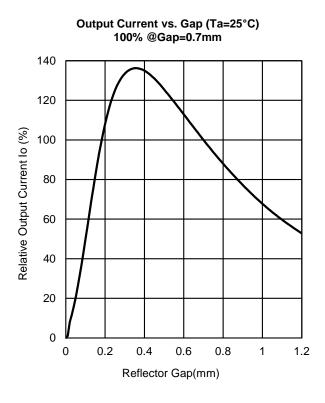


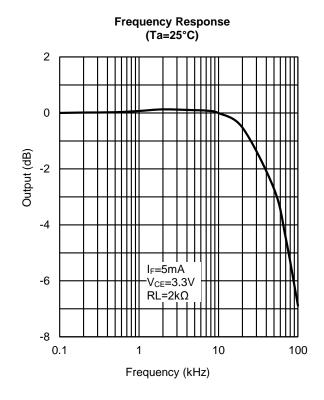


■TYPICAL CHARACTERISTICS (Ta=25°C)



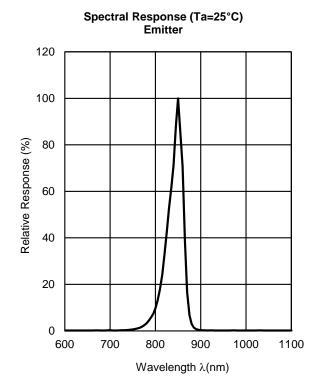


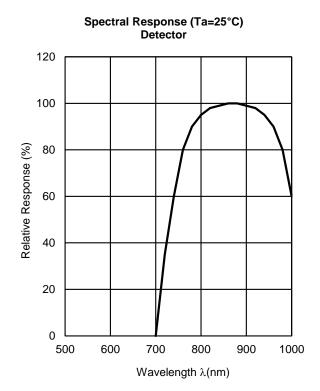






■TYPICAL CHARACTERISTICS (Ta=25°C)

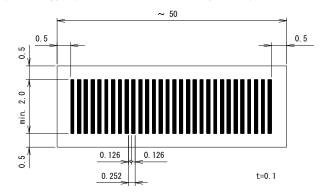






■STRIPED MIRROR OUTLINE (typ.) Unit : mm

1) Metal Type (Mirror Black : Meltec Corporation)



Outline Tolerance ±0.1mm

Dimension for Reflective Pattern 0.126mm±0.003mm

Dimension for Non-Reflective Pattern 0.126mm±0.003mm

Pattern Pitch 0.252mm



■APPLICATION NOTE

(1) Attention in handling

Treat not to touch the light receiving and light emitting part.

Avoid to adhering the dust and any other foreign materials on the light receiving and light emitting part when using. When LED has operated by voltage, it should be connected the resistor of current adjustment. Avoid to applying direct voltage to LED, because there is possibility that LED is destroyed.

When mounting, special care has to be taken on the mounting position and tilting of the device because it is very important to place the device to the optimum position to the object.

(2) Attention in designing

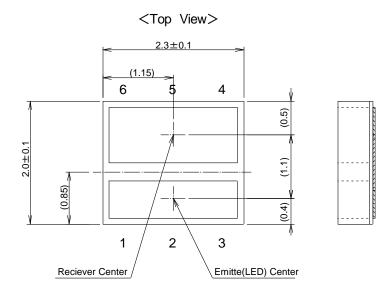
Avoid the entering ambient light into light receiving part for avoid the malfunction by ambient light. Furthermore, there is possibility of malfunction when there are the other mounted parts by near this product peripheral.

There will be changing characteristics by detection object. Refer to this datasheet and evaluate by actual detection object.

When LED has been applied continuous power on long period of time, the output current is dropped. If it uses by always applying power to LED, have to consider the circuit designing of including output current decrease.



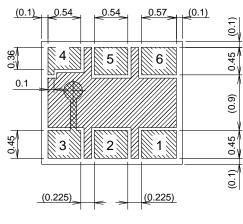
■PACKAGE OUTLINE UNIT (mm)



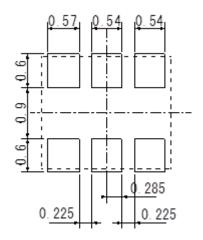


- 1. Cathode (LED)
- 2. Anode (LED)
- 3. Emitter1 (Out1)
- 4. Emitter2 (Out2)
- 5. Collector (Common Emitter1,2,3)
- 6. Emitter3 (Out3)

<Bottom View>



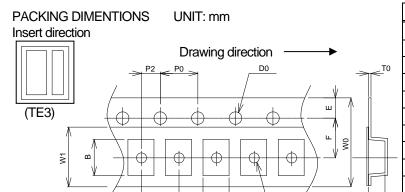
: Resist



PCB Pattern



■PACKING SPECIFICATION



Р1

SYMBOL	DIMENSION	REMARK
OTMIDOL	DIVIDITOIOIT	TIEND GIVE
Α	2.20±0.05	BOTTOM DIMENNSION
В	2.55±0.10	BOTTOM DIMENNSION
D0	φ1.50 ^{+0.1/0}	
D1	φ1.00±0.10	
Е	1.75±0.10	
F	3.50±0.05	
P0	4.00±0.10	
P1	4.00±0.10	
P2	2.00±0.05	
TO	0.20±0.05	
T1	0.80±0.10	
W0	8.00±0.20	
W1	5.40±0.10	THICKNESS 0.1MAX.

Carrier tape material: PC(Antistatic) Cover tape material: PETP(Antistatic)

■TAPING STRENGTH

There is a peel strength in the range of 0.2 to 0.7N when was peeled at a rate of 300mm per minute in opening angle 165 to 180° between the carrier tape and the cover tape.

■PACKAGING

1) The taped products are to be rolled up on the taping reel as on the drawing.

D1

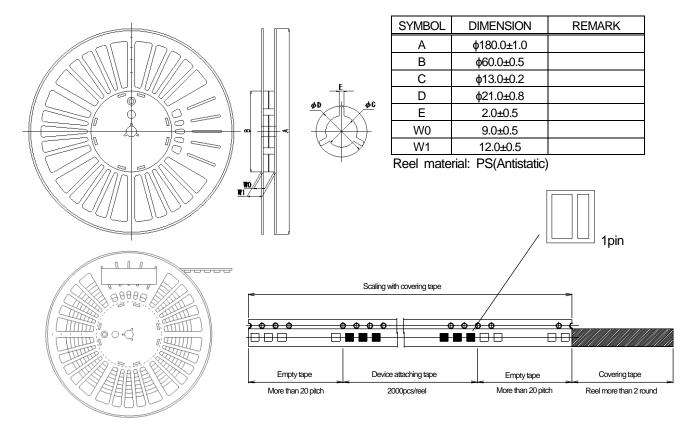
2) Rolling up specification

2-1) Start rolling : Carrier tape open space more than 20 Pieces.

2-2) End of rolling : Carrier tape open space more than 20 Pieces, and 2 round of reel space at the cover tape only.

3) Taping quantity : 2,000 Pieces

4) Seal off after putting each reels in a damp proof bag with silica gel.





■RECOMMENDED MOUNTING METHOD

NOTE

Mounting was evaluated with the following profiles in our company, so there was no problem.

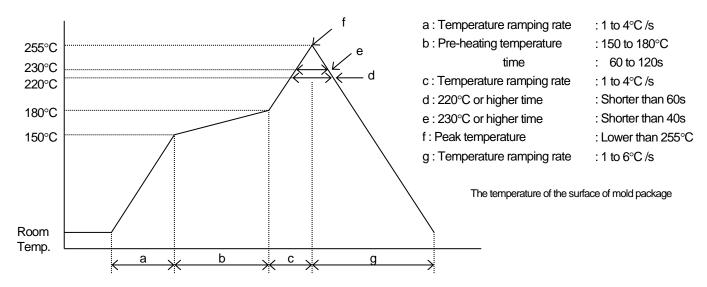
However, confirm mounting by the condition of your company beforehand.

The exposure of device under higher temperature many affect to the reliability of the products, it is recommended to complete soldering in the shortest time possible.

Mounting: Two Times soldering is allowed.

■INFRARED REFLOW SOLDERING METHOD

Recommended reflow soldering procedure



(NOTE1) Using reflow furnace with short wave infrared radiation heater such as halogen lamp

Regarding temperature profile, please refer to those of reflow furnace.

In this case the resin surface temperature may become higher than lead terminals due to endothermic ally of black colored mold resin. Therefore, please avoid from direct exposure to mold resin.

(NOTE2) Other method

Such other methods of soldering as dipping the device into melted solder and vapor phase method (VPS) are not appropriate because the body of device will be heated rapidly. Therefore, these are not recommended to apply.

(NOTE3) The resin gets softened right after soldering, so, the following care has to be taken

Not to contact the lens surface to anything.

Not to dip the device into water or any solvents.

■FLOW SOLDERING METHOD

Flow soldering is not possible.

■IRON SOLDERING METHOD

Iron soldering is not possible.



CLEANING

Avid washing the device after soldering by reflow method.

IC STORAGE CONDITIONS AND ITS DURATION

(1) Temperature and humidity ranges

Pack Sealing Temperature: 5 to 40 [°C]

Humidity: 40 to 80 [%]

Pack Opening Temperature: 5 to 30 [°C]

Humidity: 40 to 70 [%]

After opening the bag, solder products within 48h.

Avoid a dry environment below 40% because the products are is easily damageable by the electrical discharge.

Store the products in the place where it does not create dew with the products due to a sudden change in temperature.

- (2) When baking, place the reel vertically to avoid load to the side.
- (3) Do not store the devices in corrosive-gas atmosphere.
- (4) Do not store the devices in a dusty place.
- (5) Do not expose the devices to direct rays of the sun.
- (6) Do not allow external forces or loads to be applied to IC's.
- (7) Be careful because affixed label on the reel might be peeled off when baking.
- (8) The product is recommended to do the baking before using for the stability of the quality.

■BAKING

In case of keeping expect above condition be sure to apply baking.

Baking method: Ta=60°C, 48 to 72h, Three time baking is allowed

■STORAGE DURATION

Within a year after delivering this device.

For the products stored longer than a year, confirm their terminals and solderability before they are used.

■MOISTURE SENSITIVITY LEVELS

JEDEC: Level 5



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