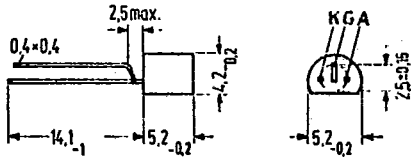


Silicon Miniature Thyristors
— SIEMENS AKTIENGESELLSCHAFT

25-11
BRY 55/30...
BRY 55/300

These diffused silicon thyristors in TO 92 plastic package (10 A 3 DIN 41868) are intended for various applications of low performance, such as controls and regulations, counters, switches, etc. These thyristors are particularly recommended for entertainment and consumer electronics.

Type	Ordering code
BRY 55/30	Q68000-A114-F10
BRY 55/60	Q68000-A183-F10
BRY 55/100	Q68000-A184-F10
BRY 55/200	Q68000-A520-F10
BRY 55/300	Q68000-A185-F10



Mounting instruction:
Fixing hole \varnothing 0.6
Approx. weight 0.25 g
Dimensions in mm

Maximum ratings ($T_j = -40$ to $+125^\circ\text{C}$;
 $R_{GK} = 1000 \Omega$)

	BRY 55/30	BRY 55/60	BRY 55/100	BRY 55/200	BRY 55/300		
Neg. and pos. repetitive peak off-state voltage	V_{RR}/V_{DR}	30	60	100	200	300	V
Max. rms on-state current	I_T (rms)	0.8	0.8	0.8	0.8	0.8	A
Surge on-state current, sinusoidal pulse	I_{TS}	8	8	8	8	8	A
$t_p < 10$ ms	I_{TS}	20	20	20	20	20	A
$t_p < 100 \mu\text{s}$	I_{TS}	20	20	20	20	20	A
Peak gate forward current	I_{GFP}	0.5	0.5	0.5	0.5	0.5	A
Repetitive reverse gate voltage	$V_{(KG)R}$	6	6	6	6	6	V
Critical rate of current rise	di/dt	100	100	100	100	100	A/ μs
$I_{TS} = 20$ A; $t_p < 10 \mu\text{s}$							
$I_{GT} = 1$ A; $t_r < 0.2$ s							
$f_{max} = 50$ Hz							
Storage temperature range	T_{stg}	-40 to +125					$^\circ\text{C}$
Junction temperature range	T_j	-40 to +125					$^\circ\text{C}$
Average gate power dissipation	$P_{G(AV)}$	0.01	0.01	0.01	0.01	0.01	W
Peak gate power dissipation	P_{GP}	0.1	0.1	0.1	0.1	0.1	W

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Static characteristics ($T_{\text{case}} = 25^{\circ}\text{C}$)Continuous reverse blocking and
off-state current ($R_{\text{GK}} = 1 \text{ k}\Omega$) $(T_j = 125^{\circ}\text{C})$ Holding current ($R_{\text{GK}} = 1 \text{ k}\Omega$) $(T_j = -40^{\circ}\text{C})$ Forward voltage ($I_{\text{TS}} = 1 \text{ A}$; $t_p = 1 \text{ ms}$)Gate-trigger current ($V_{\text{AK}} = 6 \text{ V}$; $R_L = 100 \Omega$) $(T_j = -40^{\circ}\text{C})$ Gate-trigger voltage ($V_{\text{AK}} = 6 \text{ V}$; $R_L = 100 \Omega$) $(T_j = -40^{\circ}\text{C})$

Gate non-trigger forward voltage

 $(V_D = V_{\text{DR}}$; $R_{\text{GK}} = 1 \text{ k}\Omega$; $T_j = 125^{\circ}\text{C})$

Critical rate of voltage rise

 $(R_{\text{GK}} = 1 \text{ k}\Omega$; $V = 0.67 V_{\text{DW}}$; $T_j = 125^{\circ}\text{C})$ Turn-off time ($R_{\text{GK}} = 1 \text{ k}\Omega$; $T_{\text{case}} = 125^{\circ}\text{C}$; $I_{\text{TS}(\text{rectangular})} = 1 \text{ A}$; $t_p = 50 \mu\text{s}$; $V_R = 20 \text{ V}$; $V_{\text{AK}} = 0.67 V_{\text{DR}}$; $dv/dt = 5 \text{ V}/\mu\text{s}$

Turn-on time

 $(V_D = V_{\text{DR}}$; $R_L = 100 \Omega$; $R_{\text{GK}} = 1 \text{ k}\Omega$; $I_{\text{GTS}} = 1.5 \text{ mA}$; $t_p = 2 \mu\text{s}$; $t_r = 40 \text{ ns}$)

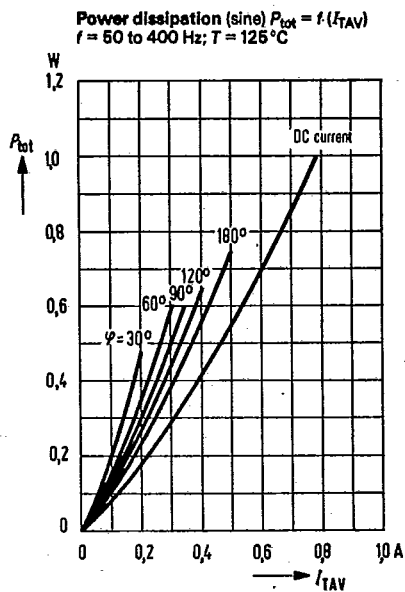
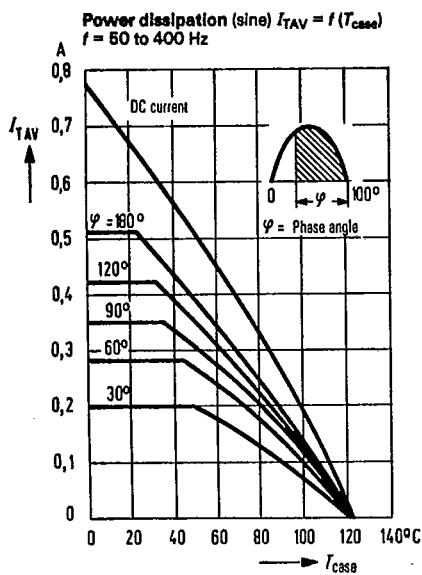
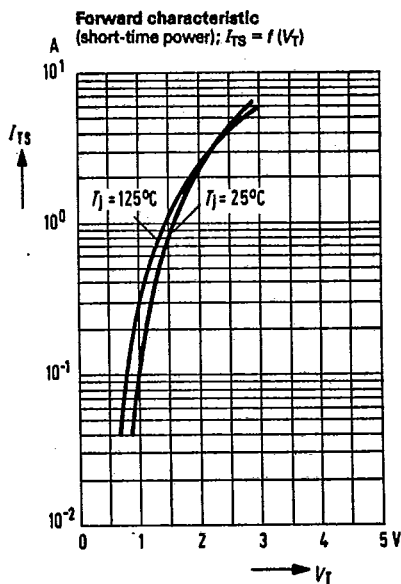
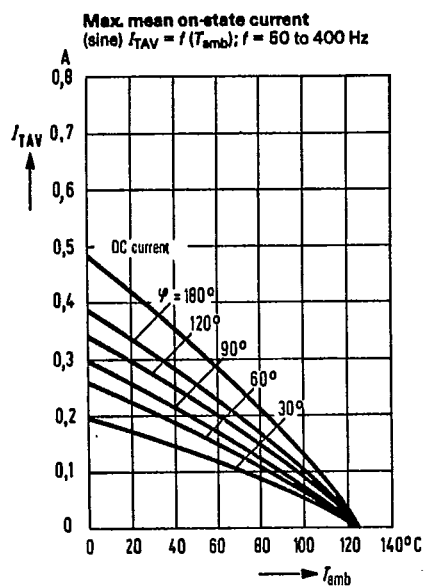
Thermal resistance

Junction to case

Junction to ambient air

	BRY 55/30 ...	BRY 55/300	
I_R/I_D	<1		μA
I_R/I_D	<50		μA
I_H	<5		mA
I_H	<10		mA
V_T	<1.5		V
I_{GT}	<200		μA
I_{GT}	<500		μA
V_{GT}	<0.8		V
V_{GT}	<1		V
V_{GF}	>0.1		V
dv/dt	10		$\text{V}/\mu\text{s}$
t_q	30		μs
t_{on}	2		μs
R_{thJC}	<125		K/W
R_{thJA}	<230		K/W

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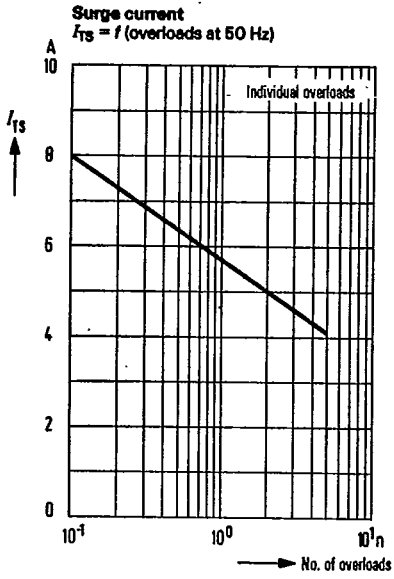


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