

T1/CEPT High Speed Jitter Attenuator

GENERAL DESCRIPTION

The function of the XR-T56189 is to attenuate the incoming jitter of clock and data. To do this, two buffers of 128 bits depth and a crystal oscillating at the receiver clock frequency are needed.

PIN ASSIGNMENT

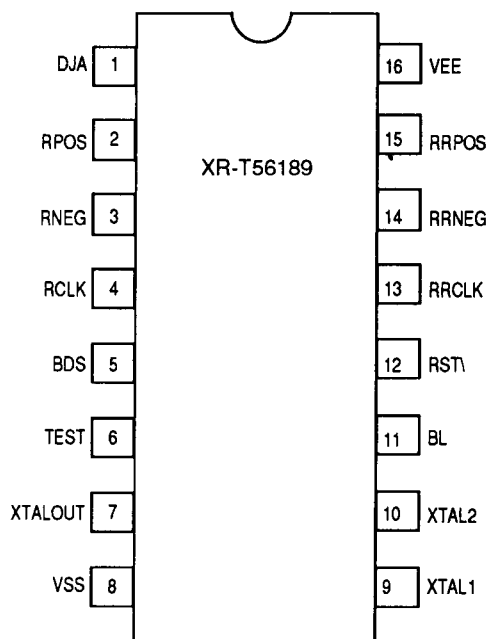
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FEATURES

- Reduces jitter present on clock and data for T1 and CEPT lines up to 128 Ulp.
- Minimum external components
- Selectable buffer size: 144, 32, 16, and 8 bits
- Disable pin option
- Single 5V +/- 10% power supply
- Pin to pin and functionally compatible to DS2188
- Maximum Data Rate (8.5MHz)
- Available in 16 pin DIP and SOIC

APPLICATIONS

- Carrier Systems
- Switching Systems
- Local Area Networks
- Multiplexers
- Cross Connects



ABSOLUTE MAXIMUM RATINGS

Supply Voltage	-1.0V to +7V
Storage Temperature	-65 C to 150 C
Soldering Temperature	260 C for 10sec

Note: Exceeding this ratings may affect reliability of this device.

ORDERING INFORMATION

Part Number	Package	Operating Temp
XR-T56188CP	Plastic	0°C to 70°C
XR-T56188IP	Plastic	-40°C to 85°C
XR-T56188CD	SOIC	0°C to 70°C
XR-T56188ID	SOIC	-40°C to 85°C