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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# M61519FP

## Audio Signal Processor with Surround

REJ03F0206-0201 Rev.2.01 Mar 31, 2008

#### **Description**

M61519FP is the semiconductor integrated circuit for Home Audio. M61519FP includes 2ch electronic volume, Tone control, 4 Input Selector, Bass Boost, REC output and Mic Mixing. This IC is suitable for Mini Component.

#### **Features**

- 2ch master volume (L, R independent control) Main volume: 0 dB to −76 dB (2 dB or 4 dB step), −∞ Trim volume: 0 dB to −15 dB (1 dB step)
- Tone control (Bass/Mid/Treble)
   ±8 dB (2 dB step)
- 4 input selector with Mute
- Surround function
- · Vocal cut function
- · Bass boost function
- L + R output for spectrum analysis display
- L + R output for subwoofer
- MIC mixing function
- 2 REC output with mute
- Input ATT: -5/0/+5 dB
   Tone input ATT: 0/-13 dB
   External input ATT: +3/0/-3/-6 dB

#### **Application**

Mini/Micro Component, Radio-Cassette Recorder with CD Player, etc.

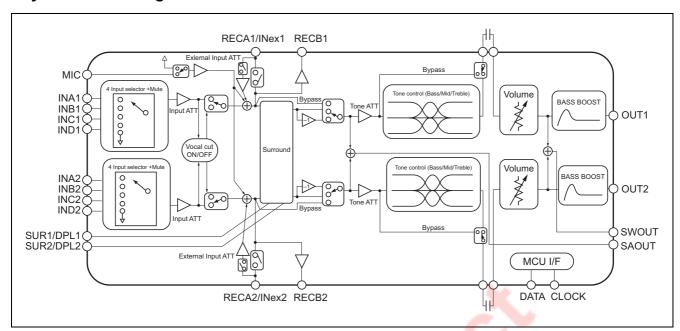
#### **Recommended Operating Conditions**

Supply voltage range: 8.0 to 10.0 V

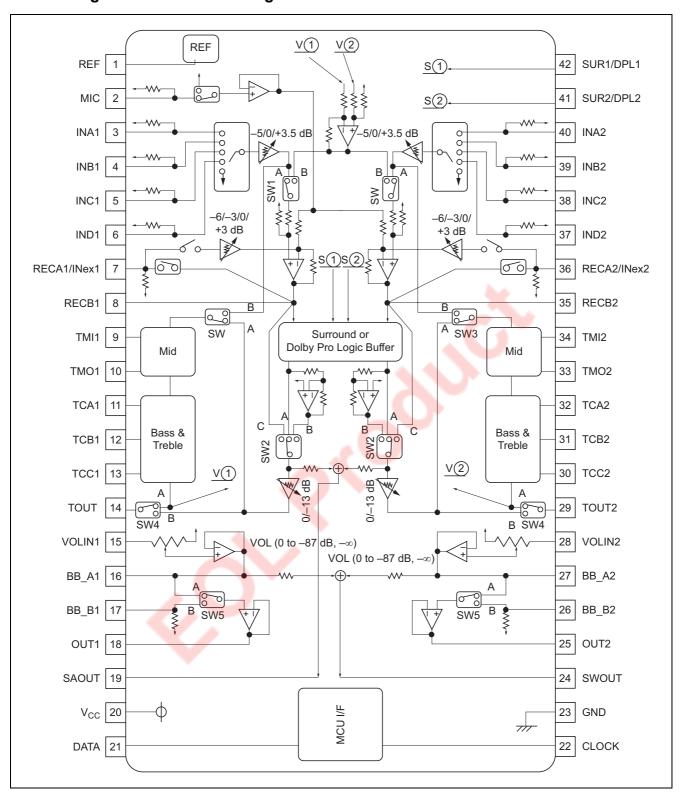
Recommended supply voltage: 9.0 V



## **System Block Diagram**



#### Pin Configuration and Block Diagram

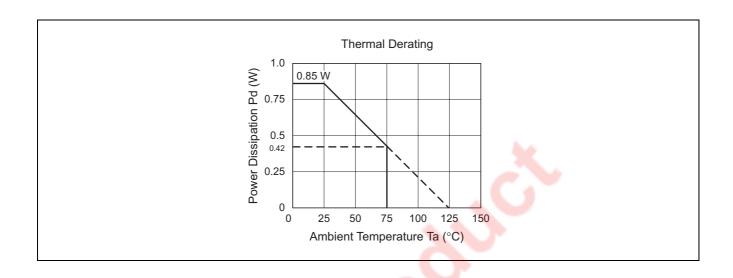


## **Pin Description**

Pin No.	Symbol	Function		
1	REF	Input pin of internal reference (REF = 4.5 V)		
2	MIC	Input pin of MIC Mixing		
3, 4, 5, 6	INA to IND	Input Selector		
37, 38, 39, 40				
41, 42	SUR/DPL	External pins for surround, Switching use with DPL Input pin		
7, 8, 35, 36	RECA/Inex, RECB	Output pins for REC, RECA can switch mute function and external Input		
		pins (At external input, RECA Switch is fixed mute position.)		
9, 10, 33, 34	TMI, TMO	External pins for Mid (sympathetic vibration type)		
11, 12, 13,	TCA, TCB, TCC	External pins for Bass/Treble (Shelving tape)		
30, 31, 32				
14, 29	TOUT	Output pins of tone control		
15, 28	VOLIN	Input pins of electronic volume		
16, 17, 26, 27	BB_A, BB_B	External pins for Bass boost		
18, 25	OUT	Output pins		
19	SAOUT	Mix pins for spectrum Analyzer (L + R/2)		
24	SWOUT	Mix pin for super woofer		
20	V <sub>CC</sub>	Internal analog, power pin for digital circuit		
23	GND	Internal analog, GND pin for digital circuit		
21, 22	DATA, CLOCK	DATA for serial data, Clock input pin		

## **Absolute Maximum Ratings**

Item	Symbol	Limits	Units	Conditions
Supply voltage	V <sub>CC</sub>	10.5	V	_
Power dissipation	Pd	850	mW	Ta ≤ 25 °C
Thermal derating	Κθ	8.6	mW/°C	Ta > 25 °C
Operating temperature	Topr	-20 to +75	°C	_
Storage temperature	Tstg	-40 to +125	°C	_

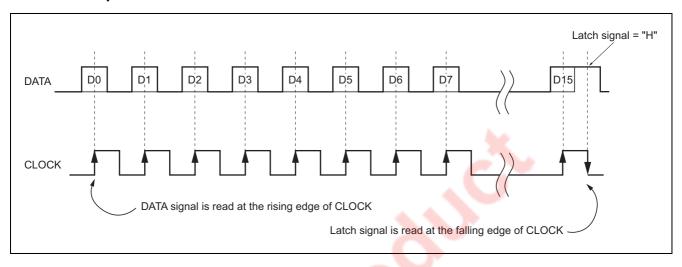


## **Recommended Operating Condition**

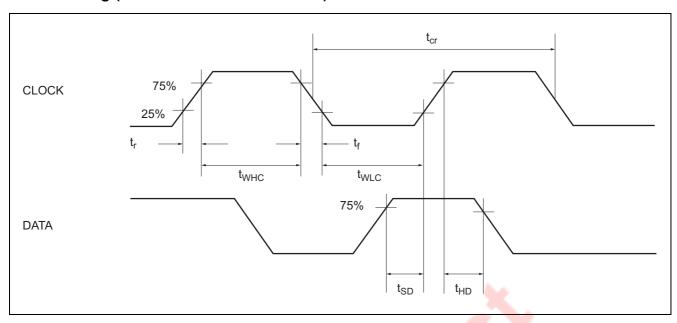
 $(Ta = 25^{\circ}C, unless otherwise noted.)$ 

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	AVDD	8	9	10	V
Logic "H" level input voltage	VIH	2.2	_	5.5	V
Logic "L" level input voltage	VIL	0	_	0.6	V

## Relationship between Data and Clock and Latch



# **Data Timing (Recommended Conditions)**



## **Digital Block Timing Regulation**

			Limits		
Item	Symbol	Min	Тур	Max	Unit
CLOCK cycle time	t <sub>cr</sub>	4	<i>-</i>	_	μS
CLOCK pulse width ("H" level)	t <sub>WHC</sub>	1.6	_	_	
CLOCK pulse width ("L" level)	t <sub>WLC</sub>	1.6	_	_	
CLOCK rise time	t <sub>r</sub>	_	_	0.4	
CLOCK fall time	t <sub>f</sub>	_	_	0.4	
DATA setup time	t <sub>SD</sub>	0.8	_	_	
DATA hold time	t <sub>HD</sub>	0.8	_	_	

## **Digital Control Specification**

Prohibit using except specified Data code as follows.

(When the power is on, the following conditions in the shaded parts are initially set.)

Input direction

[	D0	D1	D2	D3	D4	D5	D6	D7	D8	D9	D10	D11	D12	D13	D14	D15
Α	Inp	ut Selec	ctor	Inj A	out FT								0	0		
В		ne ol SW			control		Tone control Mid			Tone control Treble			0	1		
С	l	_ch Trim	ı Volume	)	F	Rch Trim Volume			Bass Boost	0	0	0	0	0	1	0
D		Lch Master Volume		Master Volume		Rch Master Volume			0	0	0	0	1	1		

#### (1) Input Selector

Selector	D0A	D1A	D2A
INA	0	0	0
INB	0	0	1
INC	0	1	0
IND	0	1	1
Mute	1	*	*

#### (2) Input ATT

Input ATT	D3A	D4A
−5 dB	0	0
0 dB	0	1
+3.5 dB	1	0

#### (3) Tone ATT

Tone ATT	D5A
−13 dB	0
0 dB	1

#### (4) Vocal Cut & Tone Bypass

Switching	D6A	D7A
Vocal cut OFF, Tone Bypass	0	0
Vocal cut OFF, Tone ON	0	1
Vocal cut ON, Tone Bypass	1	*

#### (5) MIC Mixing

MIC Mixing	D8A
OFF	0
ON	1

## (6) RECA/External Input

Switchi	D9A	D10A	D11A	
External Input Off	RECA-OFF	0	*	0
	RECA-ON	0	*	1
External Input ON	+3 dB	1	0	0
	0 dB	1	0	1
	−3 dB	1	1	0
	-6 dB	1	1	1

Note: At external Input setting, RECA is OFF.

## (7) Surround/DPLIN

Switching	D12A	D13A
Bypass	0	0
Surround	0	1
DPLIN	1	0

#### (8) Switching of Tone Control

Switching	D0B	D1B
Lch and Rch	0	0
Lch only	0	1
Rch only	1	0

#### (9) Tone Control (Bass, Mid, Treble)

Tone	D2, 6, 10B	D3, 7, 11B	D4, 8, 12B	D5, 9, 13B
+8 dB	1	1	0	0
+6 dB	1	0	1	1
+4 dB	1	0	1	0
+2 dB	1	0	0	1
0 dB	0	0	0	0
−2 dB	0	0	0	1
–4 dB	0	0	1	0
−6 dB	0	0	1	1
–8 dB	0	1	0	0

#### (10) Trim Volume (Lch and Rch)

Trim	D0, 4C	D1, 5C	D2, 6C	D3, 7C
0 dB	0	0	0	0
−1 dB	0	0	0	1
−2 dB	0	0	1	0
−3 dB	0	0	1	1
–4 dB	0	1	0	0
−5 dB	0	1	0	1
−6 dB	0	1	1	0
–7 dB	0	1	1	1
–8 dB	1	0	0	0
−9 dB	1	0	0	1
−10 dB	1	0	1	0
–11 dB	1	0	1	1
−12 dB	1	1	0	0
–13 dB	1	1	0	1
-14 dB	1	1	1	0
–15 dB	1	1	1	1

Note: Total level is fixed at –87 dB, on condition that the total level of Trim and Master volume is under "–87 dB".(example: Trim –15 dB, Master –76 dB Total level is –87 dB)

#### (11) Bass Boost

Bass Boost	D8C
OFF	0
ON	1

## (12) Master Volume (L, Rch)

Master	D0, 5D	D1, 6D	D2, 7D	D3, 8D	D4, 9D
0 dB	0	0	0	0	0
–2 dB	0	0	0	0	1
–4 dB	0	0	0	1	0
−6 dB	0	0	0	1	1
–8 dB	0	0	1	0	0
–10 dB	0	0	1	0	1
–12 dB	0	0	1	1	0
–14 dB	0	0	1	1	1
–16 dB	0	1	0	0	0
–18 dB	0	1	0	0	1
–20 dB	0	1	0	1	0
–22 dB	0	1	0	1	1
–24 dB	0	1	1	0	0
–26 dB	0	1	1	0	1
–28 dB	0	1	1	1	0
-30 dB	0	1	1	1	1
–32 dB	1	0	0	0	0
–34 dB	1	0	0	0	1
–36 dB	1	0	0	1	0
–38 dB	1	0	0	1	1 🤚
–40 dB	1	0	1	0	0
–42 dB	1	0	1	0	1
–44 dB	1	0	1	1	0
–48 dB	1	0	1	1	1
–52 dB	1	1	0	0	0
–56 dB	1	1	0	0	1
-60 dB	1	1	0	1	0
-64 dB	1	1	0	1	1
-68 dB	1	1	_ 1	0	0
-72 dB	1	1	1	0	1
-76 dB	1	1	1	1	0
–∞ dB	1	1	1	1	1

### **Electrical Characteristics**

Unless otherwise noticed, Ta = 25°C, V<sub>CC</sub> = 9 V, f = 1 kHz, Surround bypass, tone bypass and bass boost = OFF

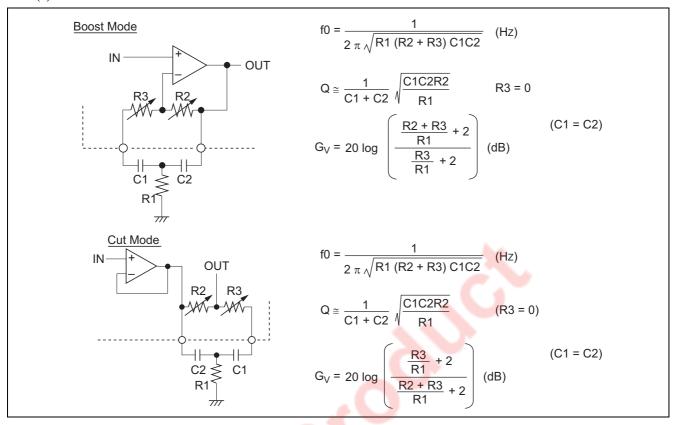
		Limits								
	Item	Symbol	Min	Тур	Max	Unit	Test Con	dition		
Voltage	Analog positive power circuit current	Alcc	_	25	40	mA	At V <sub>CC</sub> = 9.0 V, 20 pin ter signal	minal current and no		
Input and	Input resistance	Rin	30	60	90	kΩ	Input pin 3 ~ 6 pin, 37 ~ 4	10 pin, 2 pin		
Output	Maximum input	VIM	1.8	2.2	_	Vrms	3, 40 pin IN, 8, 35 pin OUT			
	voltage						RL = 10 k $\Omega$ , THD = 1%, Input ATT = -5			
	Maximum output voltage	VOM	1.8	2.2	_	Vrms	3, 40 pin IN, 18, 25 pin OUT Bass boost ON, f = 100 Hz, RL = 10 k $\Omega$ , THD = 5%			
		VrecOM	1.6	2.0	_	Vrms	3, 40 pin IN, 7, 8, 35, 36 pin OUT RL = 47 k $\Omega$ , THD = 1 %, Input ATT = +3.5 d			
	Output terminal	Vodc	4.0	4.5	5.0	V	At No signal, 18, 25 pin C	DUT		
	voltage	Vrecdc	4.0	4.5	5.0	V	At No signal, 8, 35 pin Ol	JT		
	Pass gain	Gv	-2.0	0.0	2.0	dB	Vi = 1 Vrms, FLAT, Gain ~ 25 pin, Input ATT = 0 d	·		
	Maximum attenuation	ATT	_	-92	-87	dB	Vo = 1 Vrms, 18, 25 pin OUT JIS-A, Vol = $-\infty$			
	Output noise voltage	Vono	_	10.0	20.0	μVrms	JIS-A, At no signal, $Rg = 0\Omega$	Vol = 0 dB		
				4.0	8	μVrms	18, 25 pin OUT	$Vol = -\infty$		
	Distortion factor	Vrecno	_	6.0	12	2 μVrms JIS-A, At no signal, Rg 8, 35 pin OUT		0 W		
		THD	_	0.02	0.05	%	BW: 400 - 30 kHz, Vo = 0.5 Vrms, RL = 10			
		THDrec	_	0.01	0.05	%	BW: 400 - 30 kHz, Vo = 0.5 Vrms, RL = 4			
	Cross talk between	СТ	_ <	-70	55	dB	Vo = 0.5 Vrms, RL = 10 kΩ, JIS-A, Rg = 0			
	channels	CTrec	_	-70	55	dB	Vo = 0.5 Vrms, RL = 47 k $\Omega$ , JIS-A, Rg =			
Tone	Bass voltage gain	Gbassb	6	8	10	dB	f = 100 kHz	-8 dB		
Control		Gbassc	-10	-8	6	dB		+8 dB		
	Mid voltage gain	Gmidb	6	8	10	dB	f = 1 kHz	-8 dB		
		Gmidc	-10	-8	6	dB		-8 dB		
	Treble voltage gain	Gtrebb	-2	8	10	dB	f = 10 kHz	+8 dB		
		Gtrebc	-10	-8	6	dB		-8 dB		
	Balance between channel	BALton	-2	0	2	dB	At each boost value of –8 dB and +8 dB Vo = 1 Vrms, 14, 29 pin OUT			
MIX Signal	Super woofa output gain	GvSW	-8	-6.0	-4	dB	Vi = 1 Vrms, FLAT, Gain from 3 ~ 18 pin to 40 ~ 25 pin, Input ATT = 0 dB BW: 400 - 30 kHz, Vo = 0.3 Vrms, RL = 47 15 pin IN, 24 pin OUT			
-	Distortion factor	THDSW	_	0.03	0.1	%				
	Output noise voltage	VnoSW	_	20	_	μVrms	JIS-A, At no signal, Rg = 0 $\Omega$ , 24 pin OUT  Vi = 1 Vrms, FLAT, Input ATT = 0 dB, 3 pin IN, 19 pin OUT			
	Output gain for spectrum analyzer display	GvSP	-8.0	-6.0	-4.0	dB				

Note: Mix Signal Characteristics is provided only CH1 Input. (CH2 Rg =  $0 \Omega$ )

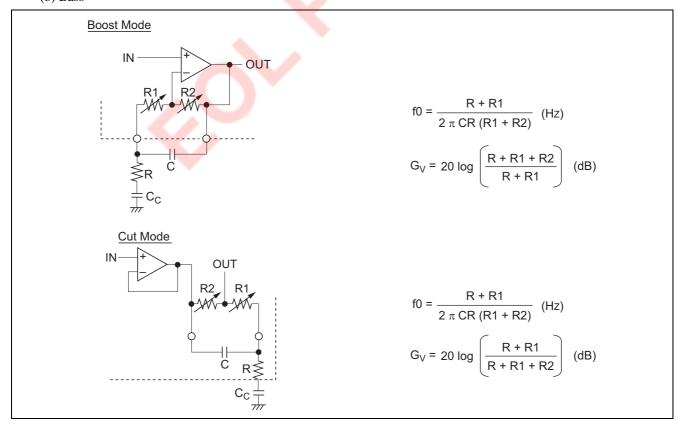
#### **Function Description**

#### (1) Equivalent circuit of tone control

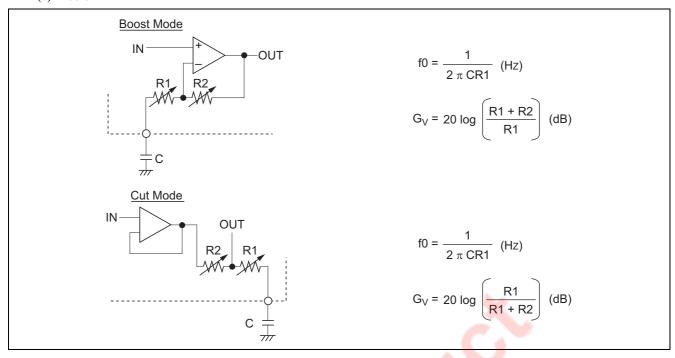
(a) Mid



(b) Bass

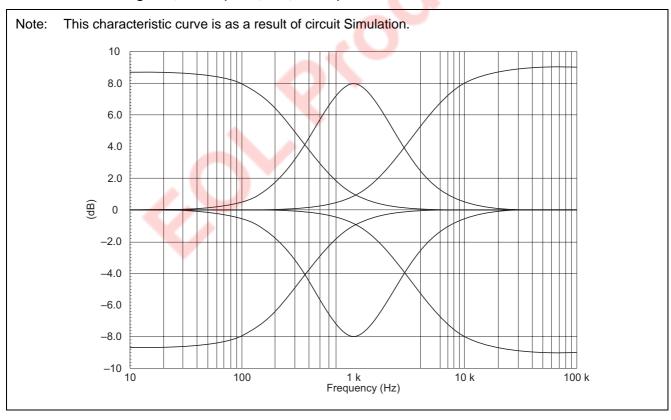


#### (c) Treble

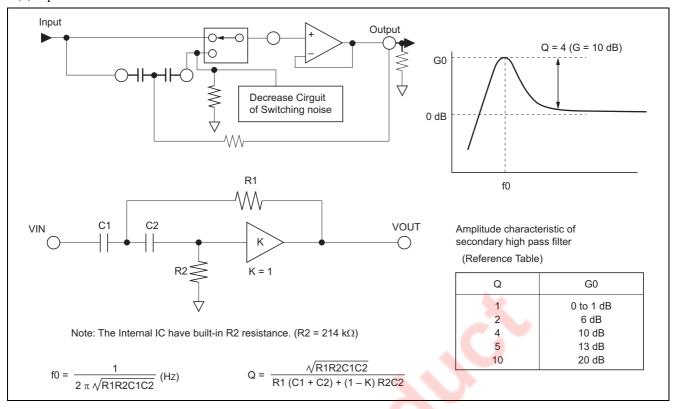


(d) Characteristic Curve of Tone Control

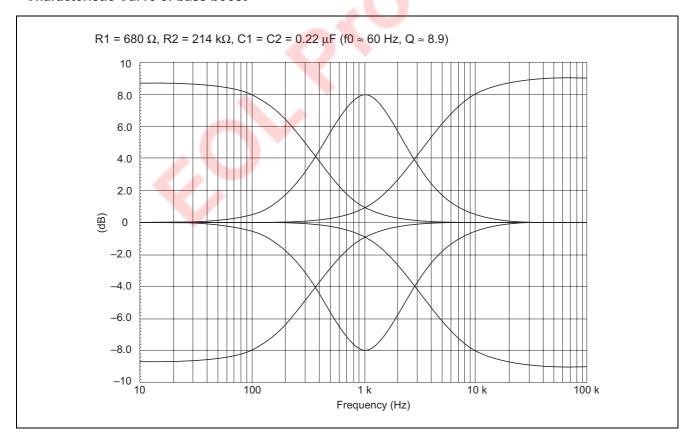
#### Tone control setting = +8, -8 dB (Bass, Mid, Treble)



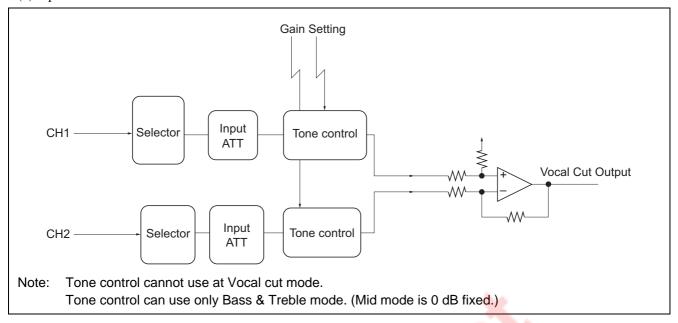
#### (2) Equivalent circuit of bass boost



#### **Characteristic Curve of bass boost**

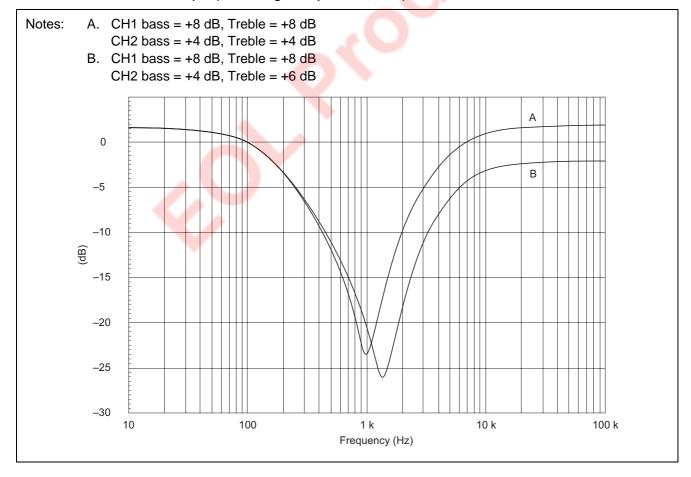


#### (3) Equivalent circuit of vocal cut

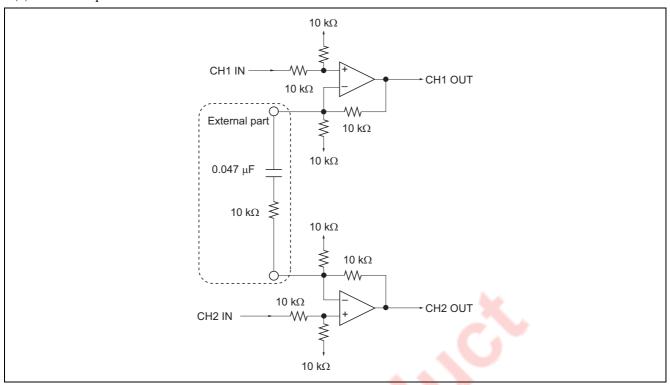


Output difference of Tone control CH1 and CH2, the characteristic to cut only Middle part of Phase Input Signal is realized.

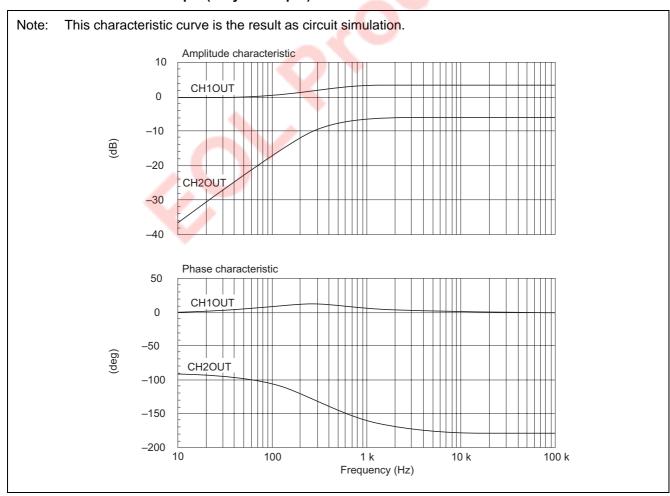
#### Characteristic curve example (Phase signal input CH1, CH2)



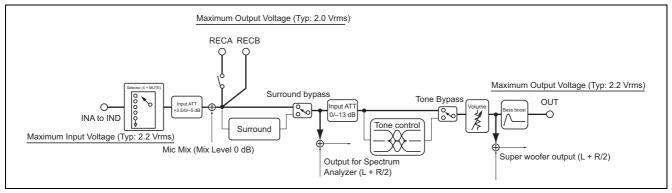
#### (4) Surround equivalent circuit



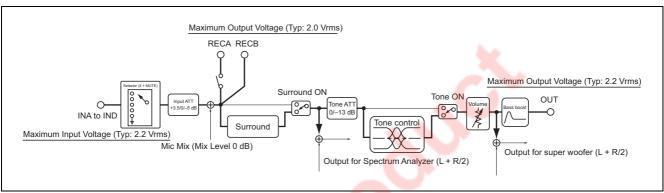
#### **Characteristic curve example (Only CH1 Input)**



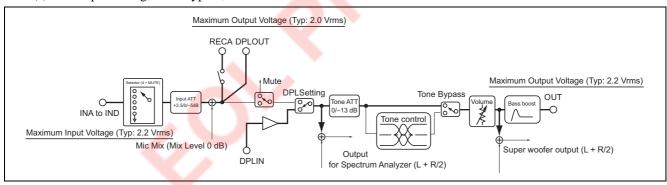
- (5) Total equivalent circuit (signal flow diagram)
  - (a) Surround bypass, Tone bypass, Bass boost ON



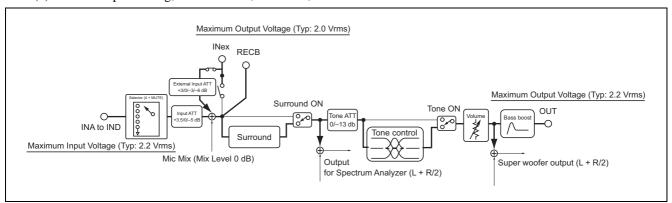
#### (b) Surround ON, Tone ON, Bass boost ON



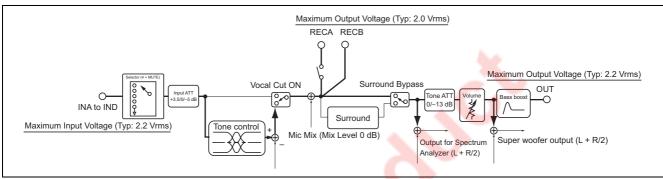
### (c) DPL Input setting, Tone bypass, Bass boost ON



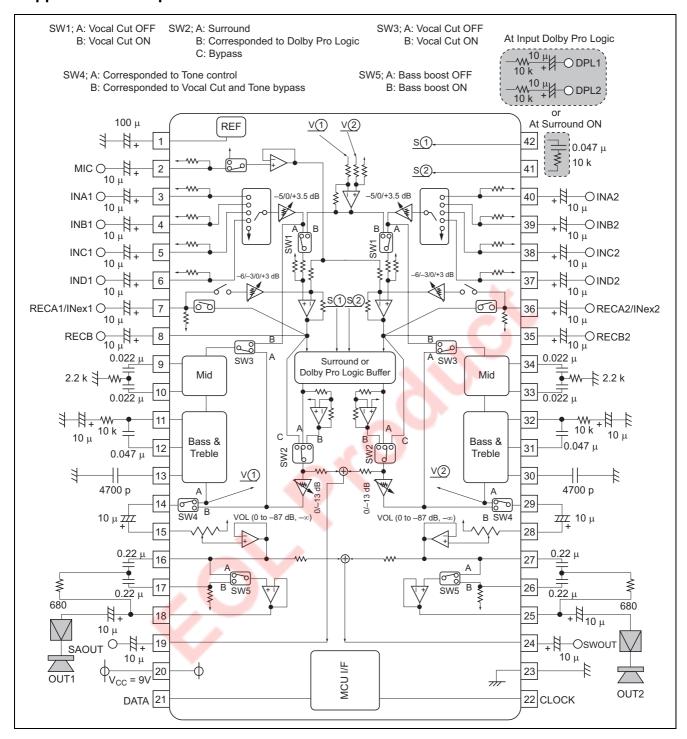
#### (d) External Input Setting, Surround ON, Tone ON, Bass boost ON



#### (e) Vocal Cut ON, Surround Bypass, Bass boost ON



#### **Application Example**



## **Package Dimensions**

# **42P2R-Δ** (MMP)

-	42P2R-A (MI)	MP)				Pla	astic 42p	in 450m	il SSOP
	EIAJ Package Code	JEDEC Code	Weight(g)	Lead Material			е	b2	
ļ	SSOP42-P-450-0.80	_	0.63	Alloy 42/Cu Alloy		->	_	<del></del>	←
						——————————————————————————————————————			2]
	뽀 삐				F	↓ -{ Re	commend	ed Mount	- Pad
	\ \ <u>\</u>				<b>1</b>	Symbol		ion in Mill	
							Min	Nom	Max
	(1)					Α	_		2.4
			(21)		Α	A1	0.05		
	<u>G</u> ⊨	D		₩	←>	A2	_	2.0	
						b	0.35	0.4	0.5
						C D	0.13 17.3	0.15 17.5	0.2 17.7
	( \( \far \dagger \)	ннннннн			A2 A1	E	8.2	8.4	8.6
				· · · · ·	712	е	0.2	0.8	0.0
	е		b			HE	11.63	11.93	12.23
	<del></del>	<u> </u>	<del>&gt;                                    </del>			L	0.3	0.5	0.7
				<b>A</b>		L1	_	1.765	_
	/ (			_	$\theta$	Z	_	0.75	_
	<del>L</del>			7		Z1	_	_	0.9
				V	<u> </u>	У	_	_	0.15
					C	$\theta$	0°	_	10°
	Z. <> Z	D-4-ii O			D-4-:1.E	b2		0.5	
	Z1 <	<u>Detail G</u>			<u>Detail F</u>	e1		11.43	

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