# NEE Crystal Clock Oscillators

PECL (Positive True ECL) COMPATIBLE (ECLiPS)

HK-2900 Series (Available from 300.0 MHz to 500.0 MHz)

HK-2910 Series (Available from 122.0 MHz to 300.0 MHz)

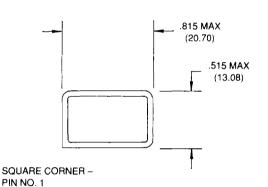


HK-2900 Series

#### **Description**

The HK-2900 & HK-2910 Series of quartz crystal oscillators provide 10E compatible signals. These devices are to operate using positive voltage signal integrity.

All units are resistance welded in an all metal package, offering RFI shielding, and are designed to survive standard wave soldering operations without damage. Insulated standoffs to enhance board cleaning are standard.



.220 MAX

(5.59)

## **Dimensions** inches (mm)

and multiple ground pins for improved



- ☐ High speed µP
- High Resolution Video
- RF Frequency Source

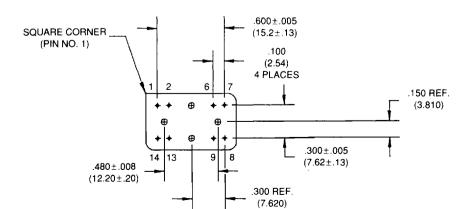
#### **Features**

- Wide frequency range -HK-2900:300.0 MHz to 500.0 MHz HK-2910:122.0 MHz to 300.0 MHz
- User specified tolerance from ±0.01% (Consult factory for tighter tolerance requirements)
- Will withstand vapor phase temperatures of 253°C for 4 minutes maximum
- All metal, resistance weld, hermetically sealed package
- High shock resistance, to 3000 G's



.250 ± .02  $(6.35 \pm .51)$ 

189 REF (4.81)



#### Pin Connections

- $V_{\text{CC}}$ 1
- Case Ground 2
- Case Ground 6
- 7 Output
- /Output 8
- Case Ground 9
- 13 Case Ground
- 14 Enable/Disable

## **Crystal Clock Oscillators**



### **PECL Operating Conditions and Output Characteristics**

		HK-2900 Series		HK-2910 Series	
PARAMETER	CONDITIONS	MINIMUM	MAXIMUM	MINIMUM	MAXIMUM
General Characteristics		(NOTE 1)		(NOTE 1)	
Supply voltage (V <sub>FF</sub> )	+5.0V	+4.75V	+5.25V	+4.75V	+5.25V
	Breakdown	-0.5V	+8.0V	-0.5V	+8.0V
Supply current (I <sub>FF</sub> )			120 mA		120 mA
Output current (I)			50 mA		50 mA
Operatir:g temperature (T <sub>a</sub> )		0°C	70°C	0°C	70°C
Storage temperature (T <sub>S</sub> )		-55°C	+125°C	-55°C	+125°C
Output Characteristics (1)					
Frequency		300.0 MHz	500.0 MHz	122.0 MHz	300.0 MHz
Overall Tolerance (2)	0-70°C	±0.01%		±0.01%	
Symmetry (3)	(50% Points)	40/60%	60/40%	40/60%	60/40%
Logic 0 (V <sub>OL</sub> ) (4)			3.41V		3.41V
Logic 1 (V <sub>OH</sub> ) (4)		3.98V		3.98V	
Rise & fall time (t,t,) (5)	(20-80% Points)		600 psec		600 psec
Jitter (6)			30 psec		30 psec

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HK-2900 Series (Available from 300.0 MHz to 500.0 MHz)

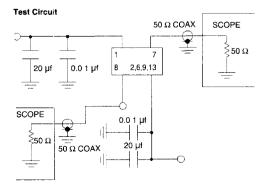
HK-2910 Series (Available from 122.0 MHz to 300.0 MHz)

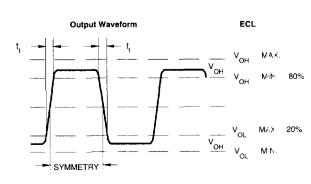
NOTE 1 - Limits are for +5.0V ±0.01V @ 25°C unless otherwise specified.

#### Footnotes:

- 1. Tested per test circuit diagram
- 2. Unless otherwise specified by customer
- 3. Reference test circuit below
- 4.  $V_{OL}$ ,  $V_{OH}$  referenced to ground
- 5. Measured between 20% and 80% of output
- 6. From rising edge to next rising edge

This information has been carefully prepared and is believed to be entirely reliable. However, no responsibility is assumed for inaccuracies. NEL reserves the right to make changes at any time in order to improve design and supply the best product possible.





TEST CIRCUIT USES A SPLIT SUPPLY OF +2V AND -3V FOR EASE OF TESTING