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#### **Ultra Low Capacitance ESD Protection** 5 V Voltage DFN 2L Unit: inch(mm) 0.042(1.05) 0.037(0.95) Features • IEC61000-4-2(ESD) : ±20kV Air, ±15kV Contact 0.026(0.65) 0.022(0.55) IEC61000-4-4(EFT) : 40A(5/50ns) IEC61000-4-5(Lightning) : 4A(8/20µS) Low leakage current, maximum of 50nA at rated voltage • Ultra low capacitance • Low clamping voltage • 0.022(0.55) Lead free in compliance with EU RoHS2.0 (2011/65/EU • & 2015/865/EU directive) 0.002(0.05)MAX. • Green molding compound as per IEC61249 Std. (Halogen Free) 0.013(0.32) 0.014(0.36) **Mechanical Data** PIN NO.1 0.022(0.55) IDENTIFICATION • Case: Molded plastic, DFN 2L Approx. Weight: 0.00004 ounces, 0.0011 gram Applications USB 3.0 Data Line Protection • Mobile Phones and accessories Hand held portable ٠ **Digital Cameras** • Computer Interfaces Protection • Serial and Parallel Ports Protection 1 2 Control Signal Lines Protection Cathode Anode

#### **Maximum Ratings**

PARAMETER	SYMBOL	VALUE	UNITS	
ESD IEC61000-4-2(Air)	Ň	±20	1.27	
ESD IEC61000-4-2(Contact)	$V_{ESD}$	±15	kV	
Operating Junction Temperature Range	ТJ	-55 to +150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C	



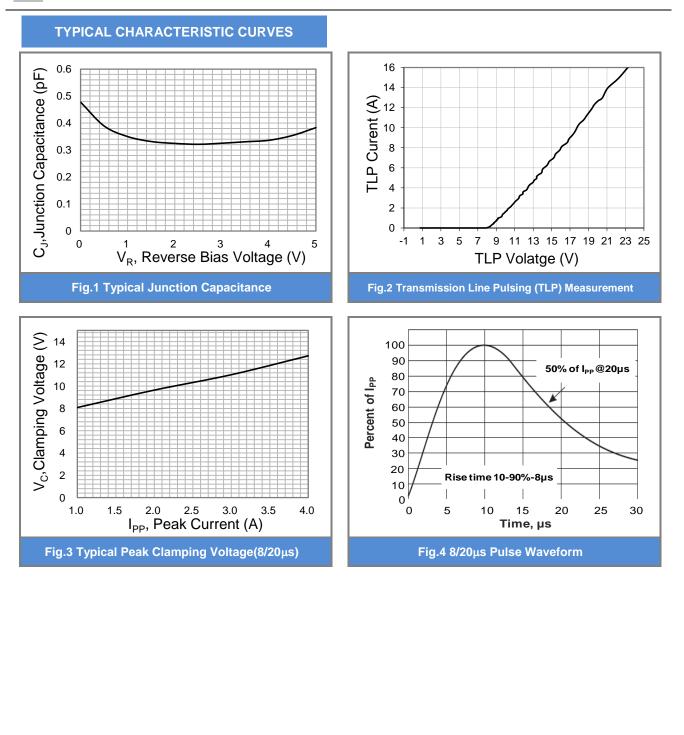
### **Electrical Characteristics**

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage (Note 1)	V <sub>RWM</sub>	-	-	-	5	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>BR</sub> =1mA	5.5	-	-	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> =5.0V	-	-	50	nA
Clamping Voltage V <sub>C</sub>		I <sub>PP</sub> =1A, t <sub>P</sub> =8/20μs	-	-	10	V
	V <sub>CL</sub>	I <sub>PP</sub> =4A, t <sub>P</sub> =8/20μs	-	-	15	V
Clamping Voltage TLP (Note 2) V <sub>CL</sub>		I <sub>PP</sub> =8A, t <sub>P</sub> =100ns	-	16	-	V
	V <sub>CL</sub>	I <sub>PP</sub> =16A, t <sub>P</sub> =100ns	-	23.5	-	V
Dynamic Resistance	R <sub>DYN</sub>	t <sub>P</sub> =100ns	-	0.94	-	Ω
Off State Junction Capacitance	CJ	2.5Vdc Bias f=1MHz	-	0.3	0.35	pF

Note :

- 1. A transient suppressor is selected according to the working peak reverse voltage(V<sub>RWM</sub>), which should be equal to or greater than the DC or continuous peak operation voltage level.
- 2. Testing using Transmission Line Pulse (TLP) conditions:  $Z0 = 50\Omega$ , t<sub>P</sub> = 100 ns.

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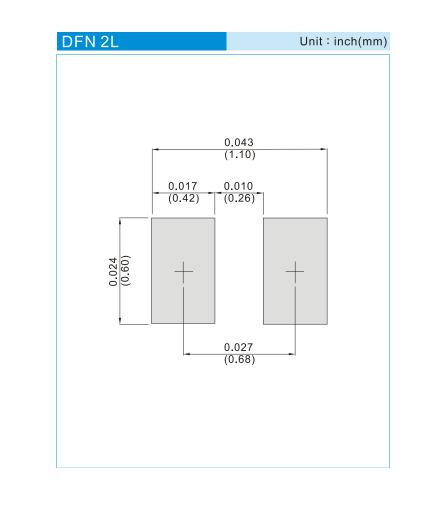




#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PE1605M1Q_R1_00001	DFN 2L	8K pcs / 7" reel	U6	Halogen free

#### **Mounting Pad Layout**





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