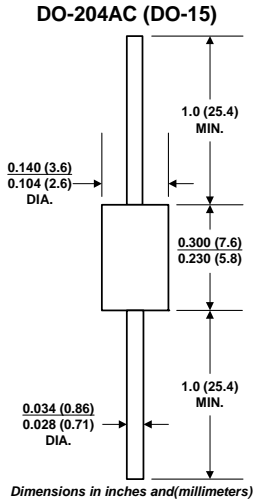


LOW CAPACITANCE TWO TERMINAL THYRISTOR SERIES

SURFACE MOUNT SMB AND AXIAL LEAD DO-15 TWO TERMINAL THYRISTOR (3T) SURGE SUPPRESSOR

FEATURES

- ⊙ UL94V-0 Flammability Classification
- ⊙ ESD Protection >40 kilovolts
- ⊙ Low Capacitance for T1/E1 Trunk and Line Card Application
- ⊙ High Surge Current Capability (See Electrical Characteristics)
- ⊙ Peak Off-State Voltage from 58 to 275 volts
- ⊙ Meet IEC61000-4-4 & -5 Industry Requirement
- ⊙ Provides Protection in Accordance with FCC Part 68 ,UL1459,Bellcore 1089,ITU-TK. 20 & K. 21
- ⊙ Pb-free plated



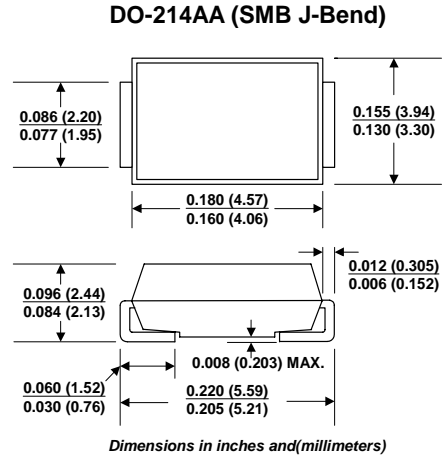
MECHANICAL DATA

Case: JEDEC DO-15 Molded plastic over passivated junction

Terminal: Solderable per MIL-STD-750, Method 2026

Mounting Position: Any

Weight: 0.015 ounce, 0.4 gram



MECHANICAL DATA

Case: JEDEC DO214AA. Molded plastic over glass passivated junction

Terminal: Solderable per MIL-STD-750, Method 2026

Standard Packaging: 12mm tape (EIA STD RS-481)

Weight: 0.003 ounce, 0.093 gram

3T PART NUMBER	MARKING CODE		REPETITIVE PEAK OFF-STAGE VOLTAGE V_{DRM} VOLTS	SWITCHING VOLTAGE @100V/us V_s VOLTS	MINIMUM HOLDING CURRENT dl/dt=1A/ms I_H mA	SWITCHING CURRENT I_s mA	SURGE RATINGS IPP 10*1000 μ S Amps	ON-STAGE CURRENT I_T A	TYPICAL CAPACITANC E @2V,1MHz pF
	FOR AXIAL DO-15 PACKAGE	FOR SMB PACKAGE							
3T064ALC	3T064ALL	GCL	58	77	150	800	50	2.2	60
3T072ALC	3T072ALL	GDL	65	88	150	800	50	2.2	60
3T080ALC	3T080ALL	GEL	75	98	150	800	50	2.2	60
3T110ALC	3T110ALL	GFL	90	130	150	800	50	2.2	60
3T130ALC	3T130ALL	GGL	120	160	150	800	50	2.2	40
3T150ALC	3T150ALL	GHL	140	180	150	800	50	2.2	40
3T180ALC	3T180ALL	GIL	160	220	150	800	50	2.2	40
3T230ALC	3T230ALL	GJL	190	260	150	800	50	2.2	30
3T260ALC	3T260ALL	GKL	220	300	150	800	50	2.2	30
3T310ALC	3T310ALL	GLL	275	350	150	800	50	2.2	30
3T064BLC	3T064BLL	GPL	58	77	150	800	75	2.2	60
3T072BLC	3T072BLL	GQL	65	88	150	800	75	2.2	60
3T080BLC	3T080BLL	GRL	75	98	150	800	75	2.2	60
3T110BLC	3T110BLL	GSL	90	130	150	800	75	2.2	60
3T130BLC	3T130BLL	GTL	120	160	150	800	75	2.2	60
3T150BLC	3T150BLL	GUL	140	180	150	800	75	2.2	40
3T180BLC	3T180BLL	GVL	160	220	150	800	75	2.2	40
3T230BLC	3T230BLL	GWL	190	260	150	800	75	2.2	30
3T260BLC	3T260BLL	GXL	220	300	150	800	75	2.2	30
3T310BLC	3T310BLL	GYL	275	350	150	800	75	2.2	30
3T064CLC	3T064CLL	HCL	58	77	150	800	100	2.2	90
3T072CLC	3T072CLL	HDL	65	88	150	800	100	2.2	90
3T080CLC	3T080CLL	HEL	75	98	150	800	100	2.2	90
3T110CLC	3T110CLL	HFL	90	130	150	800	100	2.2	90
3T130CLC	3T130CLL	HGL	120	160	150	800	100	2.2	70
3T150CLC	3T150CLL	HHL	140	180	150	800	100	2.2	70
3T180CLC	3T180CLL	HIL	160	220	150	800	100	2.2	70
3T230CLC	3T230CLL	HJL	190	260	150	800	100	2.2	60
3T260CLC	3T260CLL	HKL	220	300	150	800	100	2.2	60
3T310CLC	3T310CLL	HLL	275	350	150	800	100	2.2	60

Maximum Off-State Current @ V_{DRM} : 5 μ A

Maximum On-State Voltage @ I_T : 5volts

For AXIAL devices,use suffix L (e.g. 3T130ALL) electrical charactics apply in both AXIAL and Surface Mounted Devices.

LOW CAPACITANCE TWO TERMINAL THYRISTOR SERIES

RATINGS AND CHARACTERISTIC CURVES

(TA=25°C unless otherwise noted)

Fig.1 Pulse Wave Form Example

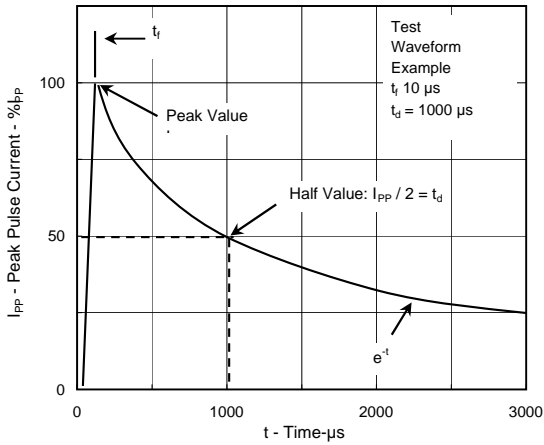


Fig.2

Typical Peak Off-State Current Vs Junction

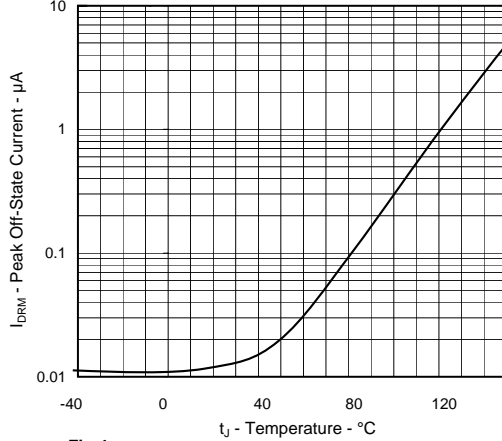


Fig.3

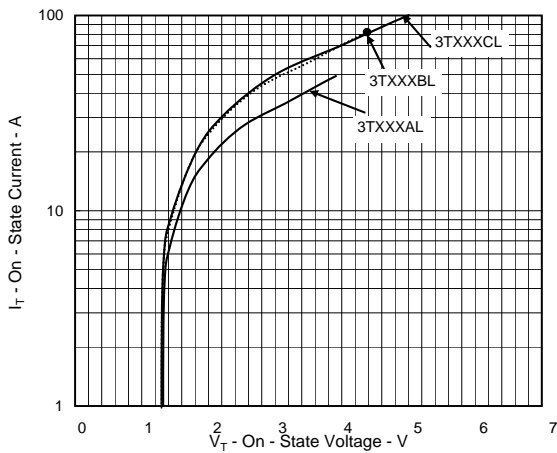


Fig.4

Typical Holding Current Vs Junction Temperature

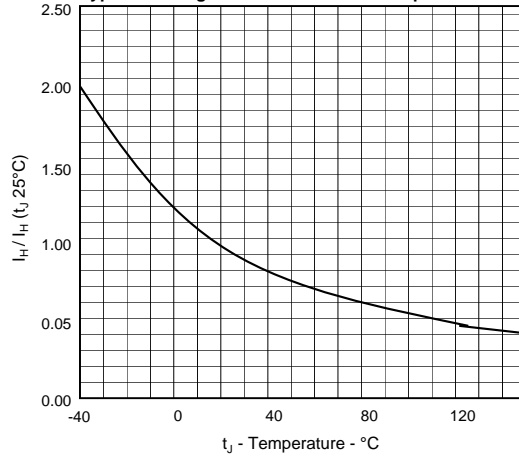


Fig.5

Typical normalized V_S Vs Junction Temperature

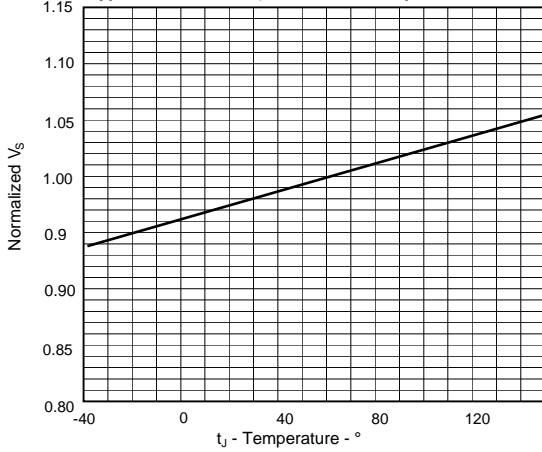


Fig.6 On-State Current Vs Surge Current

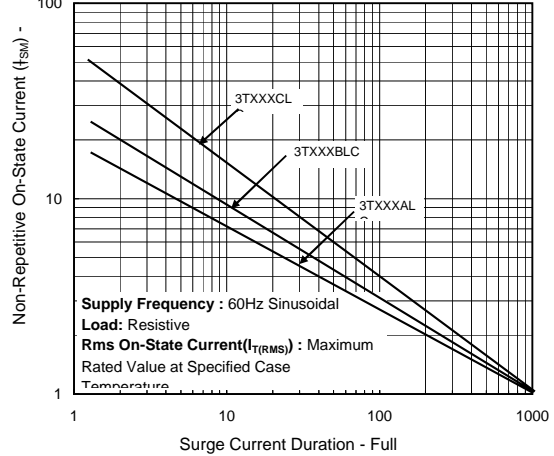


Fig.7 V - I Characteristics Curve

