High Speed CMOS Presettable Synchronous 4-Bit Binary Counters

Product Features:

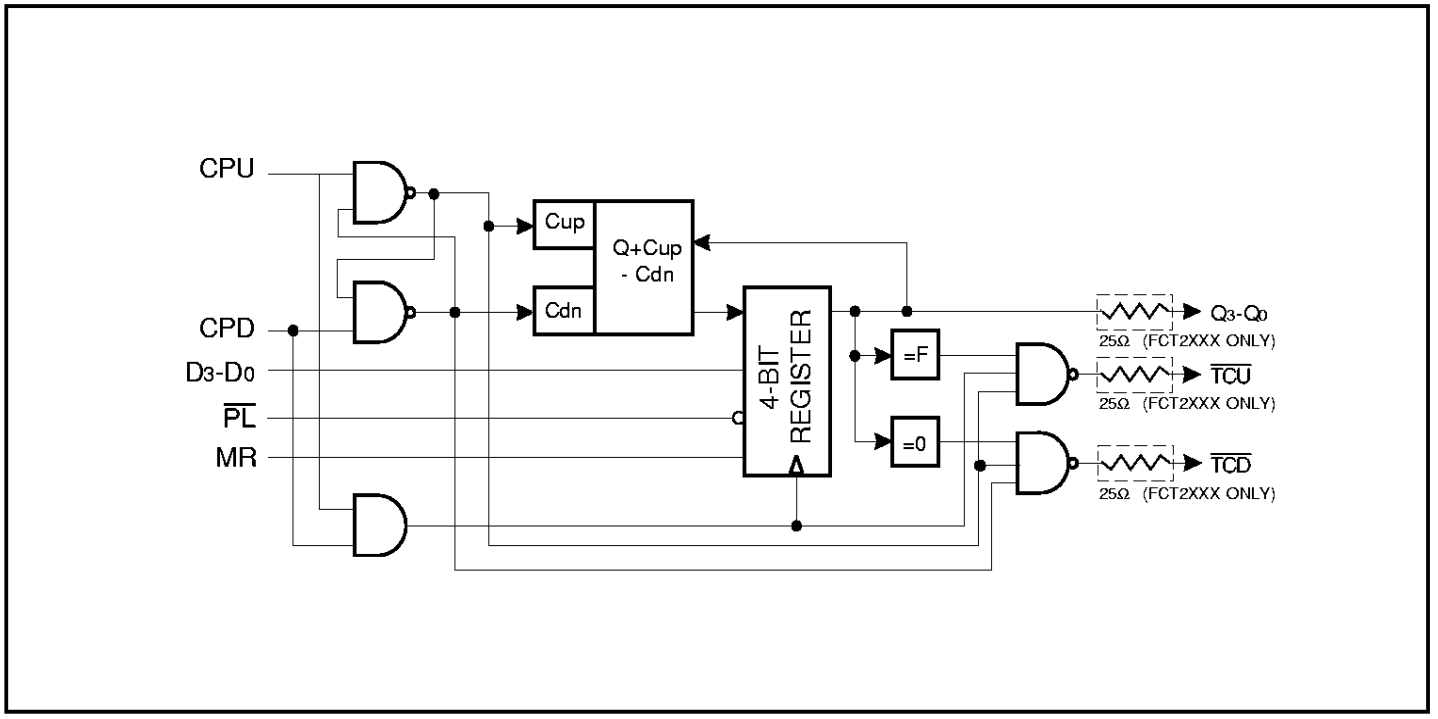
- PI74FCT193/2193T is pin compatible with bipolar FAST™ Series at a higher speed and lower power consumption
- 25Ω series resistor on all outputs (FCT2XXX only)
- TTL input and output levels
- Low ground bounce outputs (25Ω series only)
- Extremely low static power
- Hysteresis on all inputs
- Industrial operating temperature range: –40°C to +85°C
- Packages available:
  - 16-pin 150 mil wide plastic QSOP (Q16)
  - 16-pin 300 mil wide plastic SOIC (S16)

Product Description:

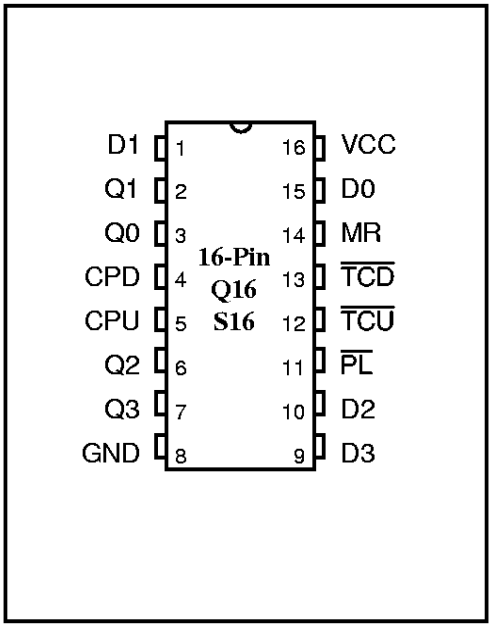
Pericom Semiconductor's PI74FCT series of logic circuits are produced in the Company's advanced 0.6 micron CMOS technology, achieving industry leading speed grades. All PI74FCT2XXX devices have a built-in 25 ohm series resistor on all outputs to reduce noise due to reflections, thus eliminating the need for an external terminating resistor.

The PI74FCT193 is a high-speed CMOS 4-bit binary up/down counter. It has separate up and down clock inputs, up/down ripple clock outputs and an asynchronous clear input. The 193 has asynchronous preload inputs which override the count inputs. All inputs have clamp diodes for undershoot noise suppression. All outputs have ground bounce suppression.

Logic Block Diagram



Product Pin Configurations (All Pins Top View)



Product Pin Description

Pin Name	Description
D3-D0	Data Inputs
Q3-Q0	Data Outputs
$\overline{PL}$	Pre Load
MR	Master Reset
CPU	Count Up Clock
CPD	Count Down Clock
$\overline{TCU}$	Terminal Count Up
$\overline{TCD}$	Terminal Count Down

Truth Table[1]

Inputs					Outputs			
$\overline{PL}$	MR	CPU	CPD	Di	Q3-Q0	$\overline{TCU}$	$\overline{TCD}$	Function
X	H	X	L	X	0000	X	L	Reset
X	H	X	H	X	0000	X	H	Reset
L	L	X	X	D3-D0	D3-D0	X	X	Load Data
H	L	-	H	X	Q+1	X	X	Count Up
H	L	H	-	X	Q-1	X	X	Count Down
H	L	L	H	X	F	L	H	Count Up = 1111
H	L	H	H	X	0-E	H	H	Count Up ≠ 1111
H	L	H	L	X	0	H	L	Count Down = 0000
H	L	H	H	X	1-F	H	H	Count Down ≠ 0000

Notes:

1. H = High Voltage Level
   L = Low Voltage Level
   X = Don't Care
   ↑ = Low to High Transition

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5.0V ± 5%)

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
V_{OH}	Output HIGH Voltage	V_{CC} = Min., V_{IN} = V_{IH} or V_{IL}	I_{OH} = –15.0 mA	2.4	3.0		V
V_{OL}	Output LOW Current	V_{CC} = Min., V_{IN} = V_{IH} or V_{IL}	I_{OL} = 48 mA		0.3	0.50	V
V_{OL}	Output LOW Current	V_{CC} = Min., V_{IN} = V_{IH} or V_{IL}	I_{OL} = 12 mA (25Ω Series)		0.3	0.50	V
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level				0.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max.	V_{IN} = V_{CC}			1	µA
I_{IL}	Input LOW Current	V_{CC} = Max.	V_{IN} = GND			–1	µA
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18 mA			–0.7	–1.2	V
I_{OFF}	Power Down Disable	V_{CC} = GND, V_{OUT} = 4.5V		—	—	100	µA
I_{OS}	Short Circuit Current	V_{CC} = Max.[3], V_{OUT} = GND		–60	–120		mA
V_H	Input Hysteresis				200		mV

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[4]	Description	Test Conditions	Typ	Max.	Units
C_{IN}	Input Capacitance	V_{IN} = 0V	6	10	pF
C_{OUT}	Output Capacitance	V_{OUT} = 0V	8	12	pF

Notes:
1. For conditions show as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 5.0V, +25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max. freq = 0	$V_{IN} \geq V_{CC}$ -0.2V $V_{IN} \leq$ 0.2V		0.1	500	µA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max. freq = 0	V_{IN} = 3.4V[3]		0.5	2.0	mA
I_{CCD}	Supply Current per Input per MHz[4]	V_{CC} = Max., Outputs Open and Enabled One Bit Toggling 50% Duty Cycle, Other inputs at GND or V_{CC}	V_{IN} = V_{CC} V_{IN} = GND		0.15	0.25	mA/ MHz
I_C	Total Power Supply Current[6]	V_{CC} = Max., Outputs Open Preset Mode $\overline{PL}$ = $\overline{CE}$= $\overline{U}$/D = CP = GND One Bit Toggling at f_i = 10 MHz 50% Duty Cycle	V_{IN} = V_{CC} V_{IN} = GND		1.5	3.5	mA
			V_{IN} = 3.4V V_{IN} = GND		1.8	4.5	
		V_{CC} = Max., Outputs Open Preset Mode $\overline{PL}$ = $\overline{CE}$ = $\overline{U}$/D = CP = GND Four Bit Toggling at f_i = 5 MHz 50% Duty Cycle	V_{IN} = V_{CC} V_{IN} = GND		3.0	6.0[5]	
			V_{IN} = 3.4V V_{IN} = GND		4.0	10.0[5]	

Notes:

1. For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.4V); all other inputs at V_{CC} or GND.
4. This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
5. Values for these conditions are examples of the Icc formula. These limits are guaranteed but not tested.
6. $I_C = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}$
   $I_C = I_{CC} + \Delta I_{CC} D_H N_T + I_{CCD} (f_{CP}/2 + f_i N_I)$
   I_{CC} = Quiescent Current
   ΔI_{CC} = Power Supply Current for a TTL High Input (V_{IN} = 3.4V)
   D_H = Duty Cycle for TTL Inputs High
   N_T = Number of TTL Inputs at D_H
   I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)
   f_{CP} = Clock Frequency for Register Devices (Zero for Non-Register Devices)
   f_i = Input Frequency
   N_I = Number of Inputs at f_i
   All currents are in milliamps and all frequencies are in megahertz.

Switching Characteristics (Over Operating Range)

Symbol	Description[1]	Conditions	193 2193 Min	193 2193 Max	193A 2193A Min	193A 2193A Max	Unit
tCPTC	Propagation Delay CPU/D to $\overline{TCU}$/$\overline{TCD}$	C_L = 50 pF R_L = 500Ω	2.0	10	2.0	6.5	ns
tCPQ	Propagation Delay CPU/D to Qi		2.0	13.5	2.0	8.8	ns
tDQ	Propagation Delay Di to Qi		2.0	15.5	2.0	10.1	ns
tPLQ	Propagation Delay $\overline{PL}$ to Qi		2.0	14	2.0	8.8	ns
tMRQ	Propagation Delay MR to Qi		3.0	15.5	3.0	10.1	ns
tMRTCU	Propagation Delay MR to $\overline{TCU}$		3.0	14.5	3.0	9.4	ns
tMRTCD	Propagation Delay MR to $\overline{TCD}$		3.0	15.5	3.0	10.1	ns
tPLTC	Propagation Delay $\overline{PL}$ to $\overline{TCU}$/$\overline{D}$		3.0	16.5	3.0	10.8	ns
tDTC	Propagation Delay Di to $\overline{TCU}$/$\overline{D}$		3.0	15.5	3.0	10.1	ns

Notes:
1. See Test Circuit and Waveforms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. This parameter guaranteed but not production tested.

Timing Characteristics (Over Operating Range)

Symbol	Description	Conditions	193 2193 Min	193 2193 Max	193A 2193A Min	193A 2193A Max	Unit
tDPLS	Di to $\overline{PL}$	C_L = 50 pF R_L = 500Ω	5.0		4.0		ns
tDPLH	Di to $\overline{PL}$		2.0		1.5		ns
tPLW	$\overline{PL}$ Low Time		6.0		5.0		ns
tCP	CPU/D Pulse Width HIGH and LOW		5.0		4.0		ns
tCPL	CPU/D Pulse Width LOW (change of direction)		10		8.0		ns
tMRH	MR High Time		6.0		5.0		ns
tRPLCR	$\overline{PL}$ to CPU/D Recovery		6.0		5.0		ns
tRMRCP	MR to CPU/D Recovery		4.0		3.0		ns

Notes:
1. See Test Circuit and Waveforms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. This parameter guaranteed but not production tested.