HCMOS 5x3.2mm 3.3V VCXO

Model:FVXO-HC53 SERIES **RoHS Compliant / Pb Free** **Rev. 9/6/2006 Preliminary**

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MODEl SEl ECTION GuIDE

F VX O - H C 5 3 b R - 155.520000

- H = HCMOS
- C = Ceramic
- 5 = 5x3.2mm
- 3 = 3.3V
- B = ±50PPM APR
- Blank = -20~+70ºC
- R = -40~+85ºC
- Frequency

• El ECTRICAI CHARACTERISTICS

PARAMETERS			MAX (unless otherwise noted)
Frequency Range	(Fo)		1.000 ~ 250.000 MHz
Absolute Pull Range (APR) [1]			±50PPM Min
Storage Temperature Range	(Tstg)		-55ºC ~ +125ºC
Supply Voltage	(VDD)		3.3V ± 5%
Control Voltage	(Vc)		1.65V ± 1.5V
Input Current	(IDD)	1.000 ~ 50.000	40mA
		50.000+ ~ 250.000	60mA
Output Symmetry (50% VDD Level)			45% ~ 55%
Rise Time (TR)	(10% ~ 90% VDD)	1.000 ~ 150.000	3nS
	(20% ~ 80% VDD)	150.000+ ~ 250.000	
Fall Time (TF)	(90% ~ 10% VDD)	1.000 ~ 150.000	3nS
	(80% ~ 20% VDD)	150.000+ ~ 250.000	
Output Voltage	(VOL)	1.000 ~ 150.000	10% VDD
		150.000+ ~ 250.000	20% VDD
	(VOH)	1.000 ~ 150.000	90% VDD MIN
		150.000+ ~ 250.000	80% VDD MIN
Output Load	(HCMOS)		15pF
Start-up Time	(Ts)		10mS
Output Enable / Disable Time [2]			100nS
Linearity			±10%
Modulation Bandwidth			10kHz Min
Maximum Soldering Temp / Time			260ºC / 10 Seconds
Moisture Sensitivity Level (MSL)			1
Termination Finish			Au

[1] Inclusive of 25ºC tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

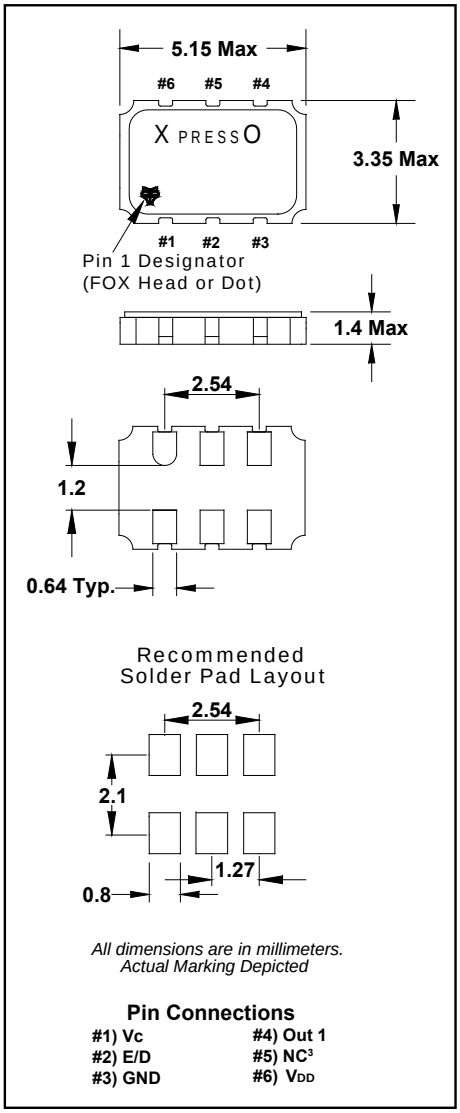
[2] An internal pullup resistor from pin 2 to pin 6 allows active output if pin 2 is left open.

[3] A complementary output version with output 2 on pin 5 is available.

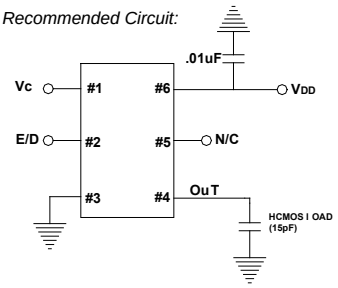
- If you have specific jitter or phase noise requirements please contact Fox Technical Support for availability.

- A 0.01µF bypass capacitor should be placed between VDD (Pin 6) and GND (Pin 3) to minimize power supply line noise.

- The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.



Recommended Circuit:



• ENAbl E / DISAbl E FuNCTION[2]	
(Pin 2)	OUTPUT (Pin 4)
OPEN	ACTIVE
'1' Level VIH ≥ 70% VDD	ACTIVE
'0' Level VIL ≤ 30% VDD	High Z

Patent Numbers:
US 6,664,860, US 5,960,403, US 5,952,890; US 5,960,405; US 6,188,290;
Foreign Patents: R.S.A. 98/0866, R.O.C. 120851; Singapore 67081, 67082; EP 0958652
China ZL 98802217.6, Malaysia MY-118540-A, Philippines 1-1998-000245
US and Foreign Patents Pending
XPRESSO® Fox Electronics

FOXElectronics 5570 Enterprise Parkway Fort Myers, Florida 33905 USA +1.239.693.0099 FAX +1.239.693.1554 http://www.foxonline.com

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• TAPE SPECIFICATIONS (millimeters)

MODEl	A	b	C	D	E	F	STD Reel QTY
FVXO-HC53	∅1.5	4.0	8.0	7.5	16.0	2.15	2,000

• REEl SPECIFICATIONS (millimeters)

MODEl	G	H	I	J	K	I	M
FVXO-HC53	2.0	∅13	∅21	∅80	∅255	17.5	2.0