

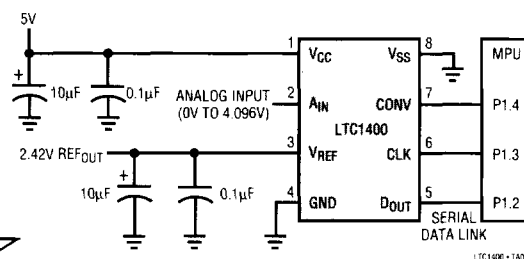
## DACs

## References

## Multiplexer/ Switches

- Design/Application
- 
- Notes

## Glossary/Tables



The diagram illustrates a 12-bit successive approximation register/parallel-to-serial converter. The input  $A_{IN}$  is connected to a switch and a capacitor  $C_{SAMPLE}$ . The output of the capacitor is connected to a 12-bit capacitive DAC. A 2.42V REF is connected to the DAC and a control logic block. The control logic also receives CLK and CONV signals. The DAC output is connected to a comparator (COMP), which is also connected to a zeroing switch. The comparator output is connected to the DOUT pin. The circuit is powered by  $V_{CC}$ , GND, and  $V_{SS}$ .

### 50μW, 4-Channel, 12-Bit ADC Samples at 200Hz

