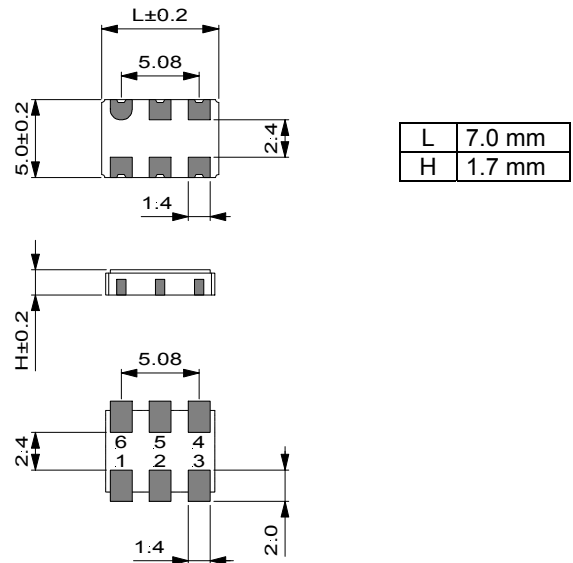


SURFACE MOUNT LVDS VCXO
DFV S11-MLLPI

FEATURES
<p>27 TO 800 MHz</p> <p>Low noise PLL multiplication circuit</p> <p>Low jitter</p>
APPLICATIONS
<p>Datcomms</p>

Function	DFV S11
V control	1
Enable/disable	2
GND	3
Output 1	4
Output 2	5
Vcc	6



TYPE	DFV S11-MLLPI
Frequency Range	27 to 800 MHz

ELECTRICAL SPECIFICATIONS	
supply voltage	3.3 V ± 5 %
supply current (no load)	≤ 80 mA
output load	100 Ω between outputs
duty cycle @ 1.25 V	45/55...55/45 %
rise/fall times (20 to 80%)	≤ 0.6 ns
high/low levels	≤ 1.6 V / ≥ 0.9 V
differential output	≤ 454 mV
differential output error	≤ 50 mV
offset voltage	≤ 1.375 V
offset voltage error	≤ 50 mV
output leakage current	≤ 10 μA
jitter RMS (12 kHz to 20 MHz)	≤ 3 ps RMS
enable / disable on pin 1	high or open = enable, low = disable
complementary output on pin 5	180° phase shifted
start up	≤ 10 ms @ 3.15 V

FREQUENCY STABILITY			tolerances [ppm]			
Type	temperature range	model code	overall stability	pulling range	function	control voltage
All models	0 to 70°	50XB25	≤ ± 25	≥ ± 50	positive	1.65 V ± 1.35 V
		100XB25	≤ ± 25			
		100XB50	≤ ± 50	≥ ± 100		
	-40 to 85°C	100XE50	≤ ± 50			
Remarks			input impedance ≥ 10 kΩ			
			includes 25°C calibration, temperature, ageing, Vcc and load changes			

ORDERING CODE	type + option code + frequency + model code
Example	DFV S11-MLLPI 622.08 MHz 100XB25