

54F126 Buffer

Quad Buffer (3-State)

Objective Specification

Military Logic Products

FEATURES

- High Impedance NPN base inputs for reduced loading (20 μ A in High and Low states)

FUNCTION TABLE

INPUTS		OUTPUT
C	A	Y
H	L	L
H	H	H
L	X	(Z)

H = High voltage level
L = Low voltage level
X = Don't care
Z = High Impedance

ORDERING INFORMATION

DESCRIPTION	ORDER CODE
Ceramic DIP	54F126/BCA
Ceramic Flat Pack	54F126/BDA
Ceramic LLCC	54F126/B2A

INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

PINS	DESCRIPTION	54F(U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
1A - 4A	Data inputs	1.0/0.033	20 μ A/20 μ A
\overline{TC} - $\overline{4C}$	3-State output enable input	1.0/0.033	20 μ A/20 μ A
1Y - 4Y	Data outputs	600/80	12mA/48mA

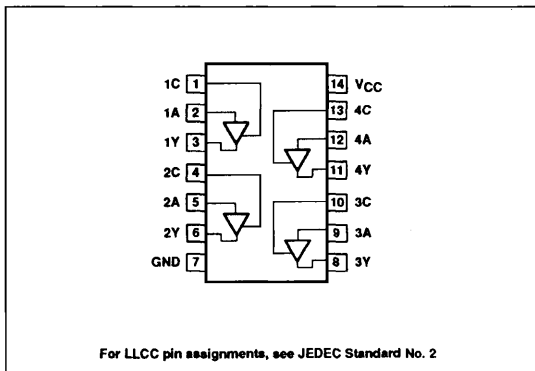
NOTE: One (1.0) FAST Unit Load is defined as: 20 μ A in the High state and 0.6mA in the Low state.

ABSOLUTE MAXIMUM RATINGS

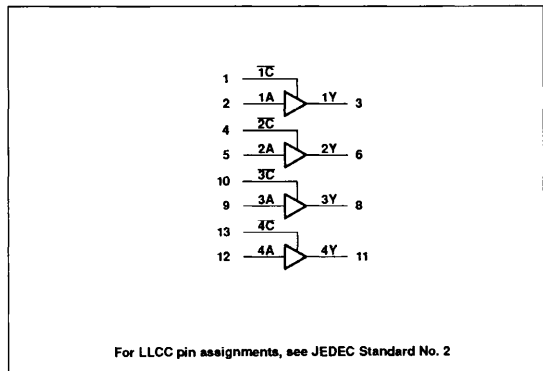
(Operation beyond the limits set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free-air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V _{CC}	Supply voltage	7.0	V
V _I	Input voltage range	-0.5 to +7.0	V
I _I	Input current range	-30 to +5	mA
V _O	Voltage applied to output in High output state range	-0.5 to V _{CC}	V
I _O	Current applied to output in Low output state	96	mA
T _{STG}	Storage temperature range	-65 to +150	°C

PIN CONFIGURATION



LOGIC SYMBOL



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RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	LIMITS			UNIT
		Min	Nom	Max	
V _{CC}	Supply voltage	4.5	5.0	5.5	V
V _{IH}	High-level input voltage	2.0			V
V _{IL}	Low-level input voltage			0.8	V
I _{IK}	Input clamp current			-18	mA
I _{OH}	High-level output current			-12	mA
I _{OL}	Low-level output current			48	mA
T _A	Operating free-air temperature range	-55		+125	°C

DC ELECTRICAL CHARACTERISTICS (Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER	TEST CONDITIONS ¹	LIMITS			UNIT	
			Min	Typ ²	Max		
V _{OH}	High-level output voltage	V _{CC} = Min, V _{IL} = Max, V _{IH} = Min	I _{OH} = -3mA	2.4		V	
			I _{OH} = Max	2.0		V	
V _{OL}	Low-level output voltage	V _{CC} = Min, V _{IL} = Max, V _{IH} = Min	I _{OL} = 48mA		.35	.50	V
V _{IK}	Input clamp voltage	V _{CC} = Min, I _I = I _{IK}		-0.73	-1.2	V	
I _{IH2}	Input current at maximum input voltage	V _{CC} = 0.0V, V _I = 7.0V			100	μA	
I _{IH1}	High-level input current	V _{CC} = Max, V _I = 2.7V			20	μA	
I _{IL}	Low-level input current	V _{CC} = Max, V _I = 0.5V			-20	μA	
I _{ozH}	Off-state output current, High-level voltage applied	V _{CC} = Max, V _{IH} = Min, V _O = 2.7V		2	50	μA	
I _{ozL}	Off-state output current, Low-level voltage applied	V _{CC} = Max, V _{IH} = Min, V _O = 0.5V		-2	-50	μA	
I _{OS}	Short-circuit output current ³	V _{CC} = Max		-100	-150	-225	mA
I _{CC}	Supply Current ⁴ (total)	I _{CC} H I _{CC} L I _{CC} Z	V _{CC} = Max	C _N = D _N = 4.5V	20	30	mA
				C _N = 4.5V, D _N = GND	32	48	mA
				C _N = GND, D _N = 4.5V	26	39	mA

AC ELECTRICAL CHARACTERISTICS (When measured in accordance with the procedures outlined in Signetics LOGIC App Note 202, "Testing and Specifying FAST Logic.")

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS						UNIT
			T _A = +25°C, V _{CC} = +5.0V			T _A = -55°C to +125°C			
			Min	Type	Max	Min	Max		
t _{PLH} t _{PHL}	Propagation delay nA to nY	Waveform 1	C _L = 50pF R _L = 500Ω			V _{CC} = +5.0V ± 10% C _L = 50pF, R _L = 500Ω			ns
			2.0	4.0	6.5	2.0	7.5		
t _{PZH} t _{PZL}	Output enable time to High and Low level	Waveform 2 Waveform 3	C _L = 50pF R _L = 500Ω			V _{CC} = +5.0V ± 10% C _L = 50pF, R _L = 500Ω			ns
			4.0	6.0	7.5	3.5	9.0		
t _{PHZ} t _{PLZ}	Output disable time From High and Low level	Waveform 2 Waveform 3	C _L = 50pF R _L = 500Ω			V _{CC} = +5.0V ± 10% C _L = 50pF, R _L = 500Ω			ns
			2.0	4.5	6.5	2.0	9.0		
			3.0	5.5	7.5	2.5	10.5		

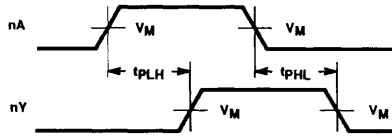
NOTES:

- For conditions shown as Min or Max, use the appropriate value specified under recommended operating conditions for the applicable type and function table for operating mode.
- All typical values are at V_{CC} = 5V, T_A = 25°C.
- Not more than one output should be shorted at a time and the duration of the short circuit should not exceed one second.
- I_{CC} is measured with outputs open.

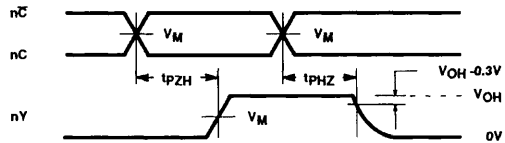
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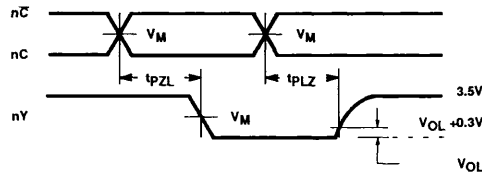
AC WAVEFORMS



Waveform 1. Propagation Delay For Input to Output



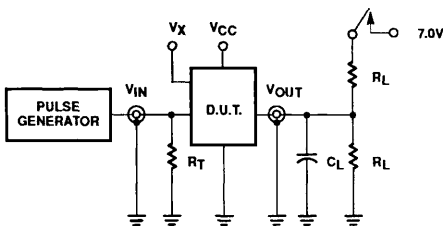
Waveform 2. 3-State Output Enable Time to High Level and Output Disable Time from High Level



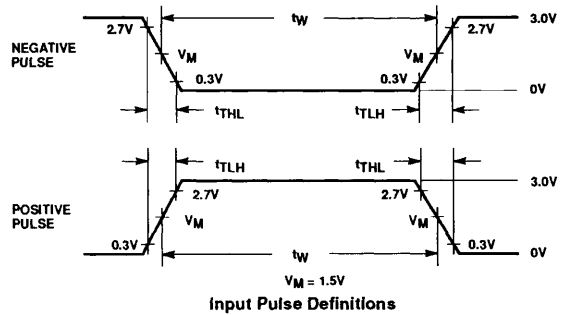
Waveform 3. 3-State Output Enable Time to Low Level and Output Disable Time from Low Level

NOTE: For all waveforms, $V_M = 1.5V$

TEST CIRCUIT AND WAVEFORM



Test Circuit for 3-State Outputs and Open Collector Outputs



SWITCH POSITION

TEST	SWITCH
t_{PLZ}	closed
t_{PZL}	closed
All other	open

INPUT PULSE CHARACTERISTICS

Family	Rep. Rate	Pulse Width	t_{TLH}	t_{THL}
54F	1MHz	500ns	$\leq 2.5ns$	$\leq 2.5ns$

DEFINITIONS:

- R_L = Load Resistor; see AC Characteristics for value.
- C_L = Load capacitance includes jig and probe capacitance; see AC Characteristics for value.
- R_T = Termination resistance should be equal to Z_{OUT} of pulse generators.
- V_X = Unlocked pins must be held at: $\leq 0.8V$; $\geq 2.7V$ or open per Function Table.